## UV-Initiated Surface Preparation and Reaction on Semiconductor Wafer Surfaces

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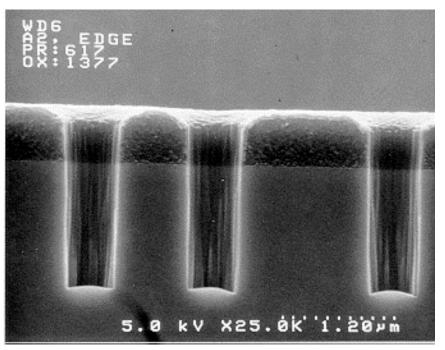
# Outline

- UV-initiated surface preparation – UV/Cl<sub>2</sub> metals removal
- Experimental Setup
- UV-initiated surface reactions
   UV-enhanced Atomic Layer Deposition (ALD)
- Summary

# Sources of Metallic Contamination

- Photoresist
- Reactive Ion Etching (RIE) (plasma etching)
- Oxygen Ashing
- Replace HPM (SC-2) with UV/Cl<sub>2</sub>

SEM of 0.5 µm Feature After RIE



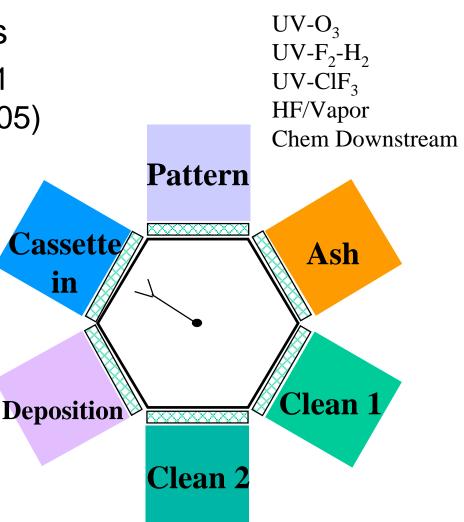
 $CD = 0.5 \ \mu m$ AR = 2.5 HDP Source, post-RIE, before ashing PR / TEOS

### Front End Cleaning Steps

Contaminant	Application	Liquid Phase	Gas Phase
Organics	• Post-RIE	<ul> <li>O<sub>2</sub> Ash</li> </ul>	• UV-O <sub>3</sub>
	• Ion Implant	<ul> <li>SPM (Piranha)</li> </ul>	• UV-Cl <sub>2</sub>
	Rework	<ul> <li>Ozonated Water</li> </ul>	• Moist O <sub>3</sub>
Oxide	<ul> <li>Pre-gate</li> </ul>	• Dilute HF	• HF/vapor
Particles	<ul> <li>Post-CMP</li> </ul>	<ul> <li>APM (SC-1) + megasonics</li> <li>APM (SC-1) + brush scrubbing</li> </ul>	<ul> <li>Cryogenic Aerosol</li> <li>Laser</li> </ul>
Metals	<ul> <li>Post-RIE</li> </ul>	• HPM (SC-2)	<ul> <li>UV-O<sub>3</sub></li> <li>UV-Cl<sub>2</sub></li> </ul>

# Motivation — UV/Cl<sub>2</sub> Metal Removal

- No surface tension effects
  - Vapors penetrate sub-0.1 micron features (year 2005)
- No contamination from liquid bath
- Cluster tools
  - Wafer protected from atmosphere
  - No worker exposure



### ESH Significance —UV/Cl<sub>2</sub> Metal Removal

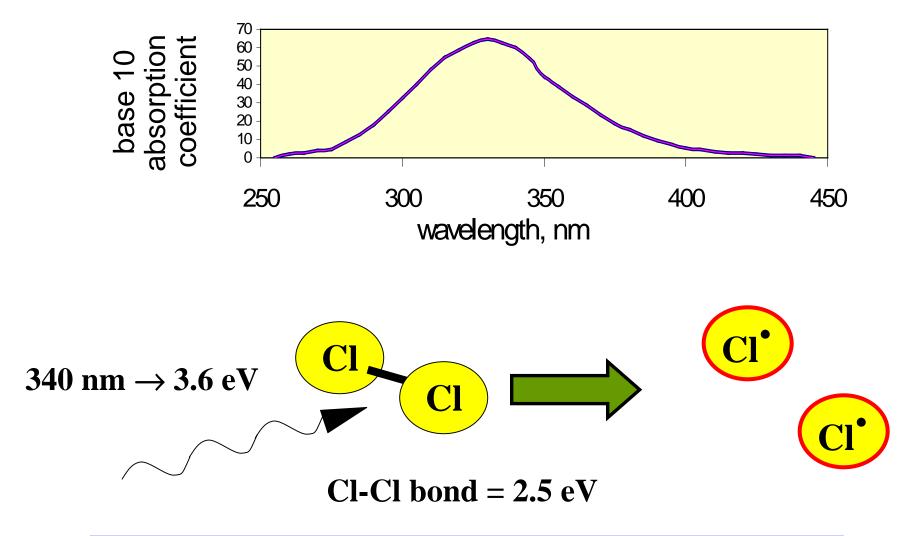
#### **Benefits**

- Eliminate aqueous cleans:
  - Reduced consumption of HCI/H<sub>2</sub>O<sub>2</sub>/H<sub>2</sub>O
  - Less liquid waste
  - Conserve UPW
- Reduced worker exposure

#### Concerns

- New kinds of waste
- Chlorine
- Ultraviolet exposure

### Background Information—UV/Cl<sub>2</sub> Metal Removal

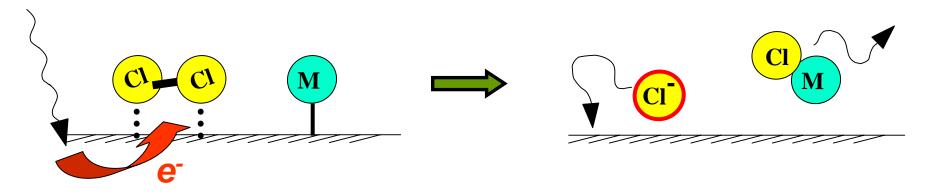


### Project Objectives—UV/Cl<sub>2</sub> Metal Removal

- Verify UV/Cl<sub>2</sub> as clean
- Removal mechanism
  - "Lift-Off"
  - Volatile products
- Reaction mechanism and products
  - Gas-phase or surface photolysis
  - Substrate and dopants
  - Oxide thickness
  - Contaminant and its concentration
  - Surface termination and other adsorbed species
  - Ultraviolet wavelength
- Monochromatic UV source

### Project Objectives—UV/Cl<sub>2</sub> Metal Removal

• UV-initiated surface reactions on Si and SiO<sub>2</sub>.



- Relate electron-hole pairs to wavelength and dopant concentration.
- Model system for semiconductor/adsorbate/photon interactions.

## International Technology Roadmap for Semiconductors

 Table 21 1999 Short Term Surface Preparation Technology Requirements

Year of Introduction	2001	2002 130 nm	2003	2004	2005 100 nm	
Front End of Line (A)						
Critical surface metals (at/cm <sup>2</sup> )	≤6x10 <sup>9</sup>	≤4.4x10 <sup>9</sup>	≤3.4x10 <sup>9</sup>	≤2.9x10 <sup>9</sup>	≤2.5x10 <sup>9</sup>	
Metal atoms per Si(100)	1:323,000				1:770,000	
Solutions Exist		Solutions Being		No Known		

### Critical Surface Metals: Fe<sup>1,4</sup>,Ca, Co, Cu<sup>1,3,4</sup>, Cr, K<sup>1</sup>, Mo, Mn, Na<sup>1</sup>, and Ni<sup>1,2,4</sup>

Pursued

1. Sugino, et al. 2. Courtney and Lamb 3.Opila, et al. 4. Lawing, et al.

NSF/SRC Engineering Research Center for Environmentally Benign Semiconductor Manufacturing

Solution

## Thermochemistry

Reaction	$\Delta G_{rxn}$ (250°C)	P <sub>sub</sub> (250°C)
$Cu + Cl(g) \rightarrow CuCl$	-219 kJ/mol	4x10 <sup>-5</sup> Torr
$Cu + 2Cl(g) \rightarrow CuCl_2$	-322 kJ/mol	4x10 <sup>-7</sup> Torr
$Cu_2O + 2CI(g) →$ 2CuCl + ½ $O_2(g)$	-307 kJ/mol	4x10 <sup>-5</sup> Torr
$CuO + CI(g) \rightarrow CuCI + \frac{1}{2}O_2(g)$	-112 kJ/mol	4x10 <sup>-5</sup> Torr

## **UV Reactor Schematics**



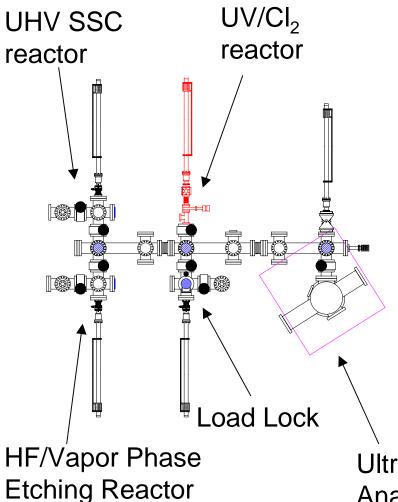
#### **Present Reactor**

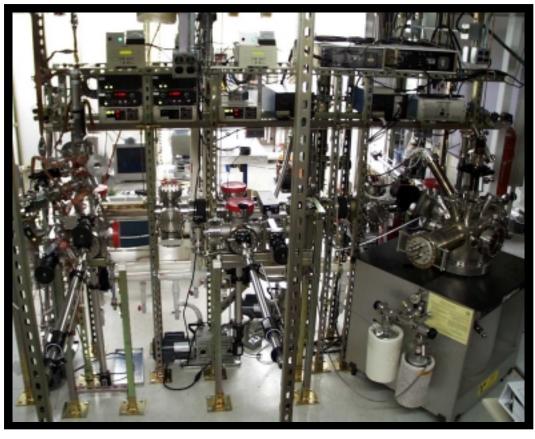
- Temperature (100 250°C) and pressure (25 - 500 mTorr) controlled
- 15 50 sccm Cl<sub>2</sub> flowrate
- 250-Watt Hg arc lamp

#### **Future Reactor**

In-situ Fourier Transform
 Infrared Spectroscopy (FTIR)

## **Integrated Processing Apparatus**



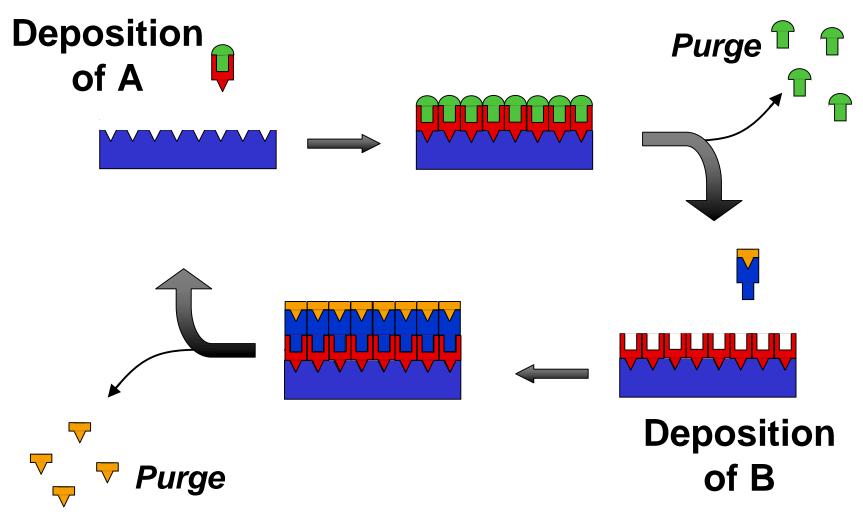


Ultra High Vacuum Surface Analysis Chamber

# Outline

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## Schematic of Atomic Layer Deposition



# UV Atomic Layer Deposition

#### Thermal ALD

- 1/10th of a layer per cycle
- High temperature Process
  - 500°C
  - processing conflicts
- Pulse and Purge

### UV ALD

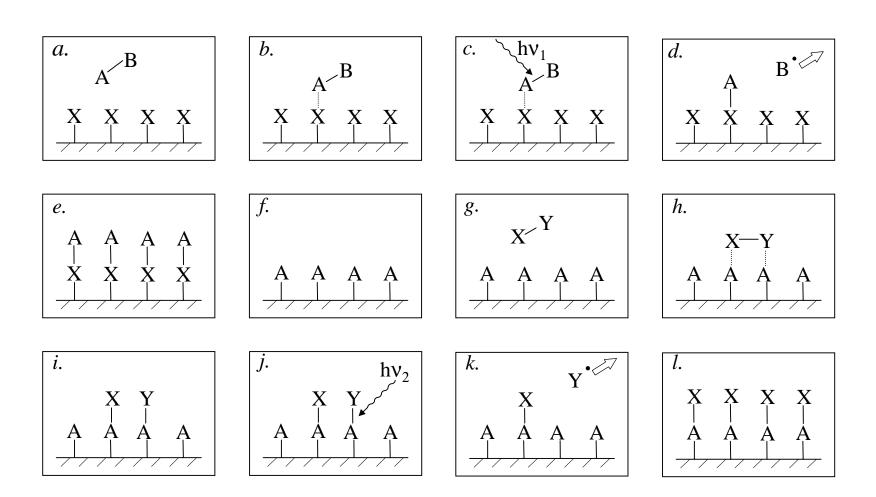
- 1 layer per cycle
  - Improved deposition rate
- Lower temperature
  - High-K dielectric layers,
     Diffusion barriers

- Reaction triggered by UV illumination.
  - Eliminate pulse-purge cycles
  - Decrease consumption

# Project Objectives — UV Atomic Layer Deposition

- Precursor sequences for deposition of ZrO<sub>2</sub>
- Reduced processing temperatures
- Mechanisms and kinetic parameters
- Multiple precursors simultaneously
- Resist-free patterning

# Possible UV-Enhanced Deposition Sequence



# Summary

### UV/Cl<sub>2</sub> Metal Removal

- Nickel, copper, iron, sodium, potassium and organic contaminants
- Reaction and removal mechanisms
- Monochromatic light source

#### **UV-Enhanced Atomic Layer Deposition**

- Identify and test precursor combinations
- Grow high quality, commercially useful films at low temperature
- Both precursors present simultaneously

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