

Practical and Theoretical Considerations for Cu e-CMP*

Alan West

**Department of Chemical Engineering, Columbia University
acw17@columbia.edu**

*Presented at ECS meeting, Quebec, May 2005

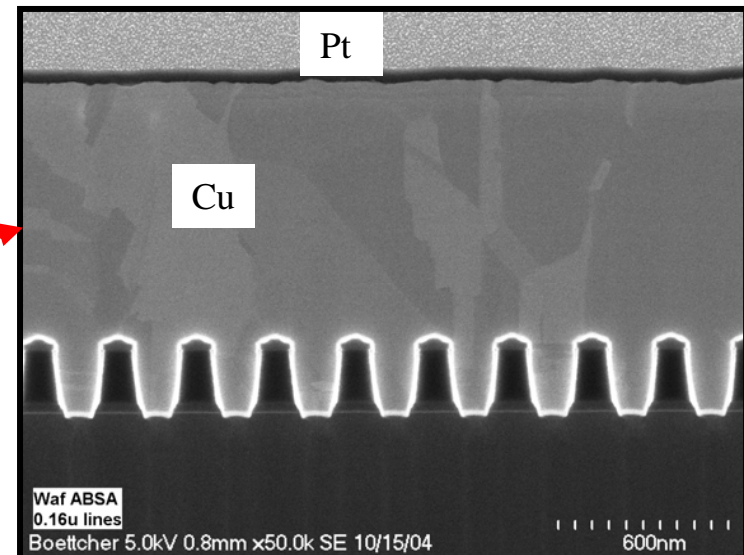
Why e-CMP?

low down-force replacement of or
complement to CMP

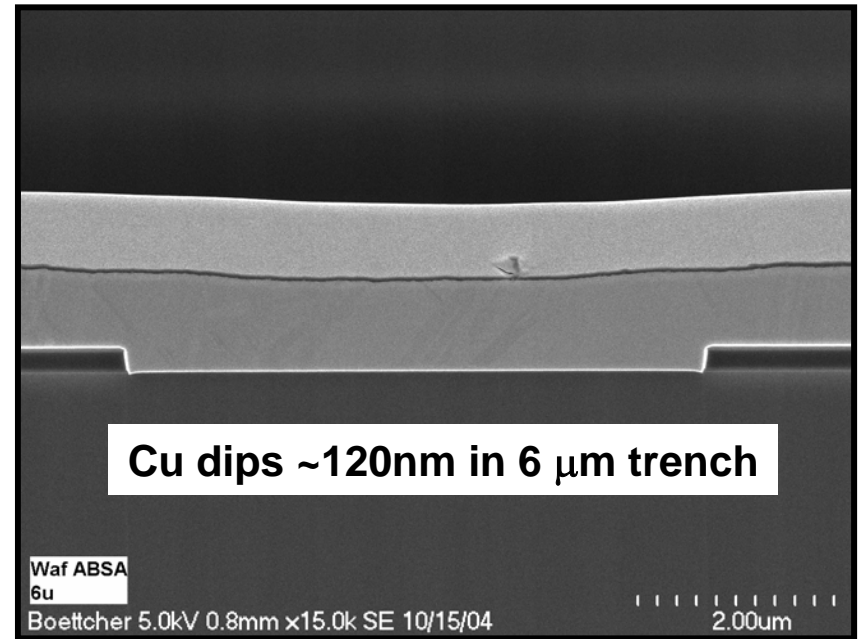
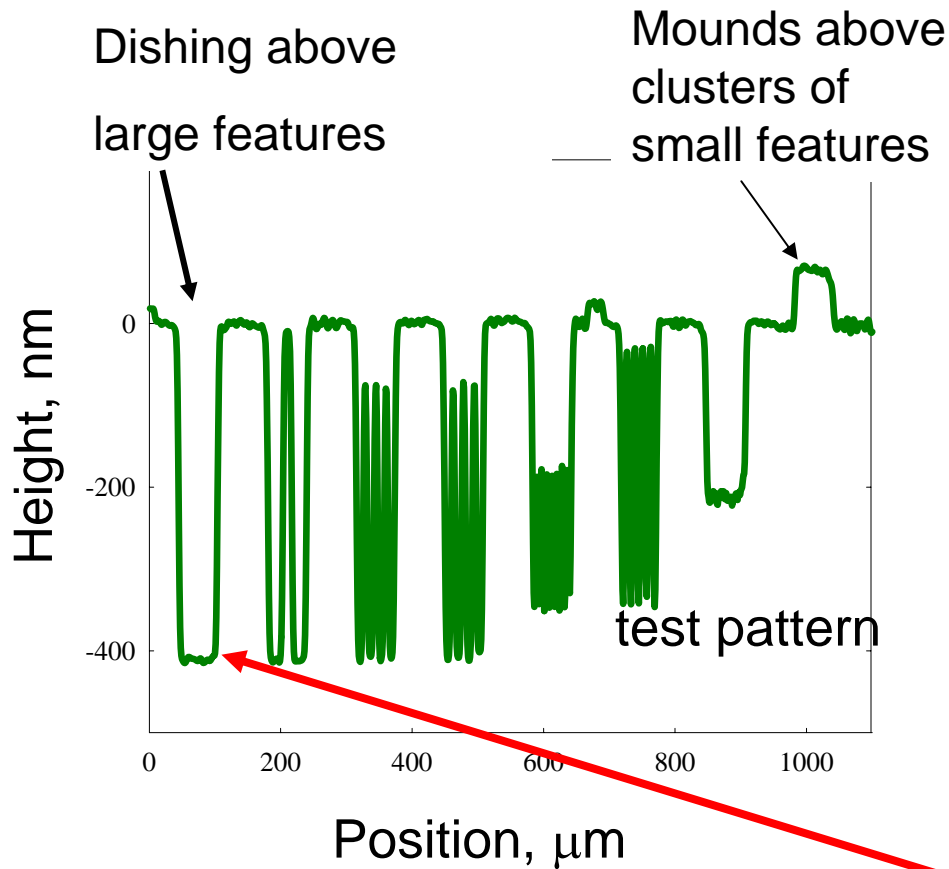
Goals:

- remove partially or completely overburden
- planarize
- fewer defects than CMP
- achieve high throughput

overburden

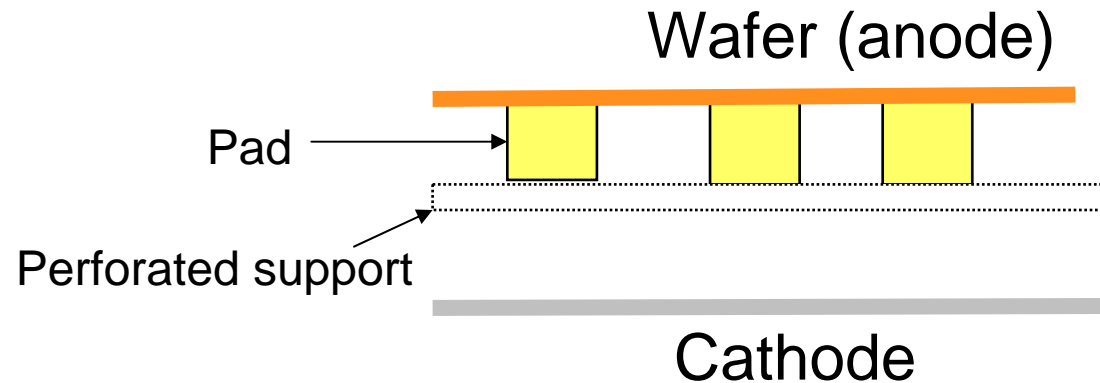


The Challenge: As-Deposited Overburden Topography



$$s_o / w_f = 0.008$$

What is e-CMP?

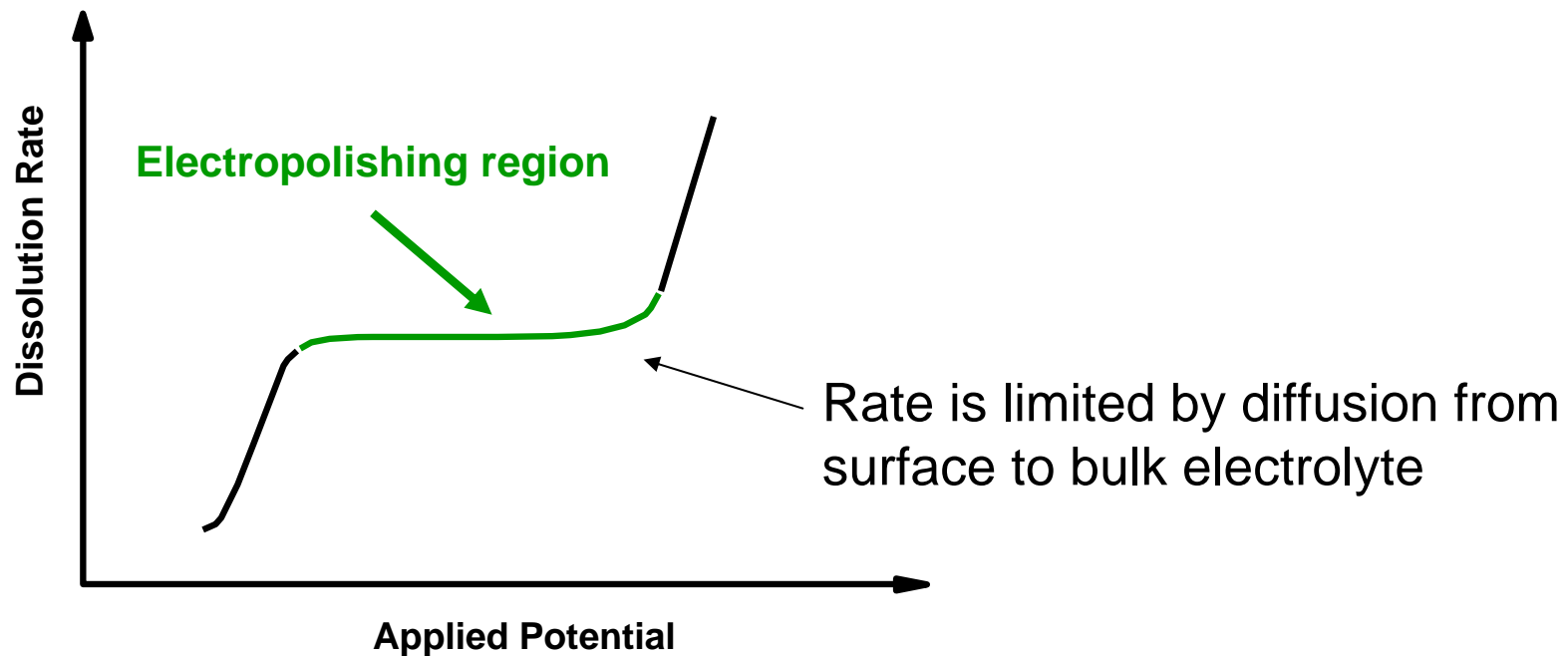


- Couples electrochemical oxidation of Cu with polishing pad
- Electrons supplied by external circuit to oxidize Cu
In CMP, oxidizing agent is in the electrolyte

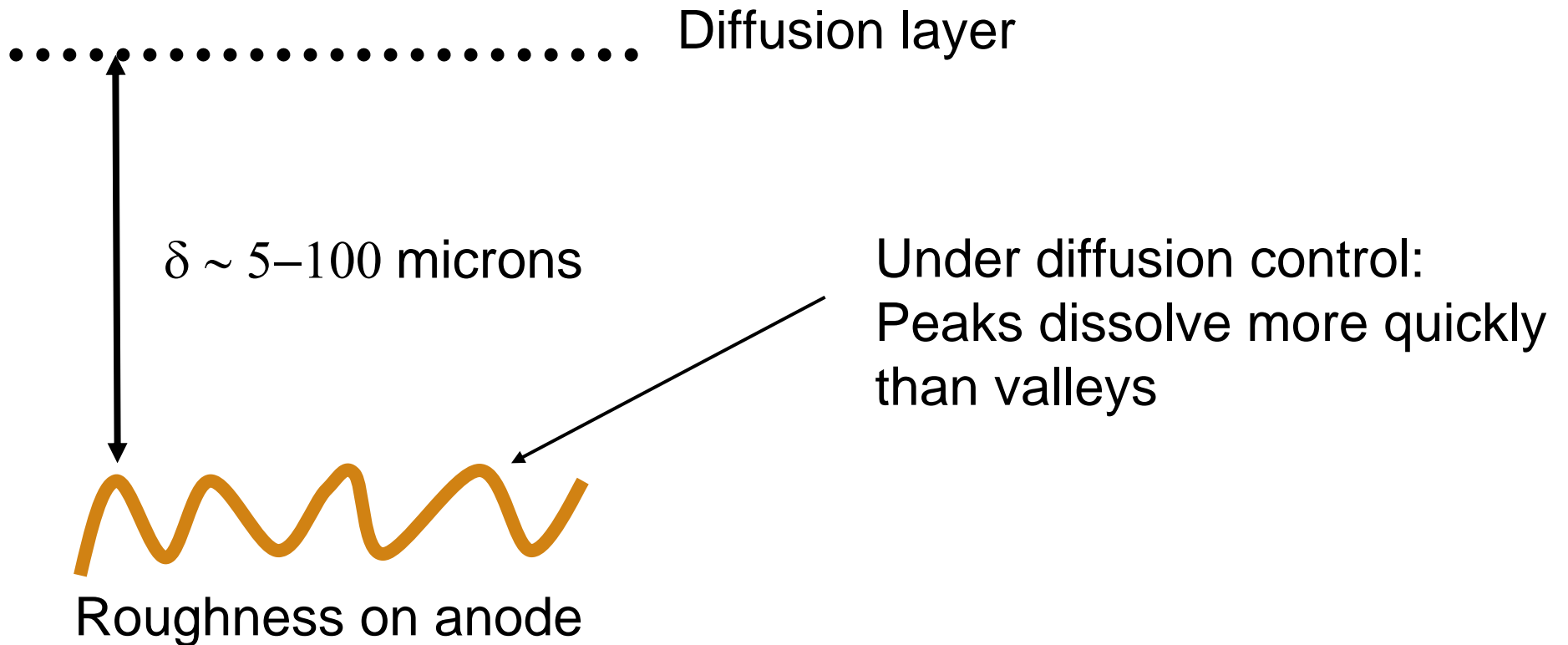
Electropolishing Electrolytes as Starting Point for e-CMP?

- Assumed to be in most early work

Reviewed in: Alan C. West, Panos Andricacos, and Lili Deligianni, "Electrochemical Polishing of Metallization," *IBM J. Res. Devel.*, **49**, 37 (2005).



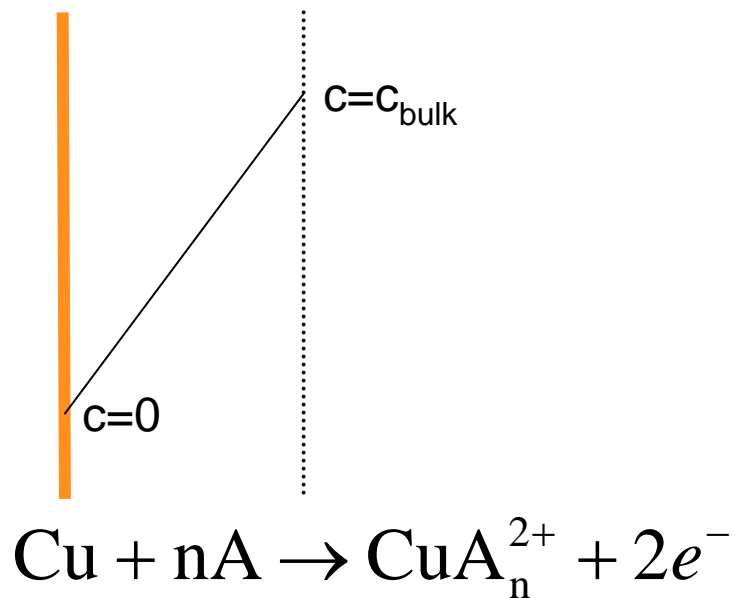
Electropolishing Requires Transport Control



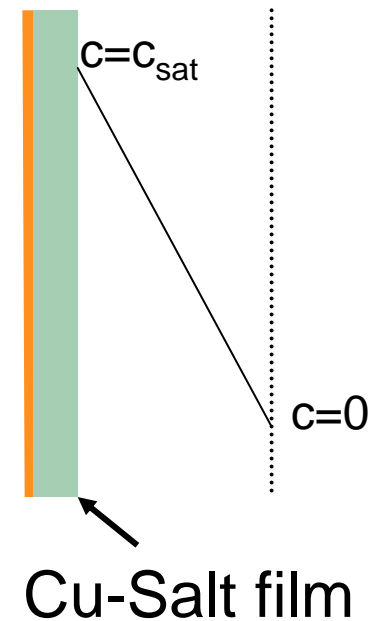
What is source of Diffusion Control?

- Diffusion-limited rates only achievable in select electrolytes
- Two primary mechanisms have been identified

Acceptor Mechanism



Salt-Film Mechanism



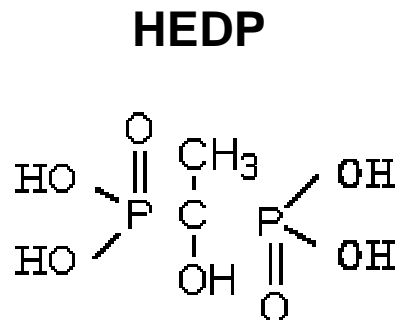
Mechanism for Cu

- Phosphoric acid: acceptor mechanism

Roberto Vidal and Alan C. West, *J. Electrochem. Soc.*, **142**, 2689 (1995) and *J. Electrochem. Soc.*, **142**, 2682 (1995).

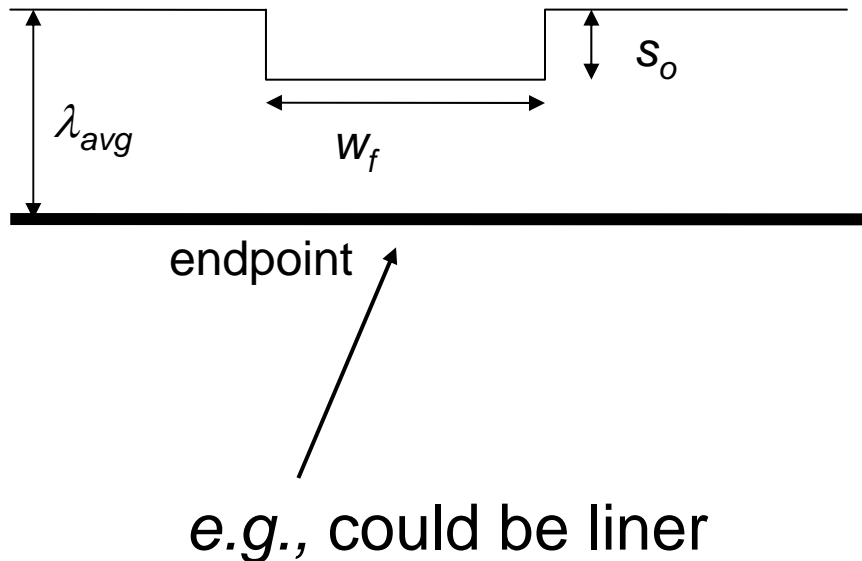
- HEDP: salt-film mechanism claimed

J. Huo, R. Solanki, and J. McAndrew, *J. Appl. Electrochem.*, in press (2004).



Numerical Simulation of Electrochemical Planarization

- Used to evaluate viability of electrochemical polishing
Results normally do not depend on mechanism

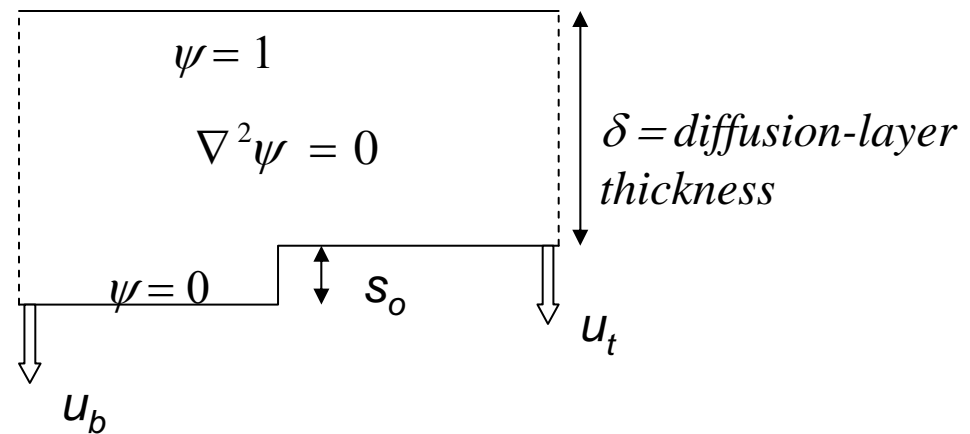


Results shown for reduction in step height as function of material removed

Numerical Simulation of Electrochemical Planarization

Simulation summary:

1. assume idealized geometry of single feature with initial step height s_o
2. Calculate normalized concentration ψ , assuming diffusion-controlled dissolution rate
3. Move surface, tracking changes in step height



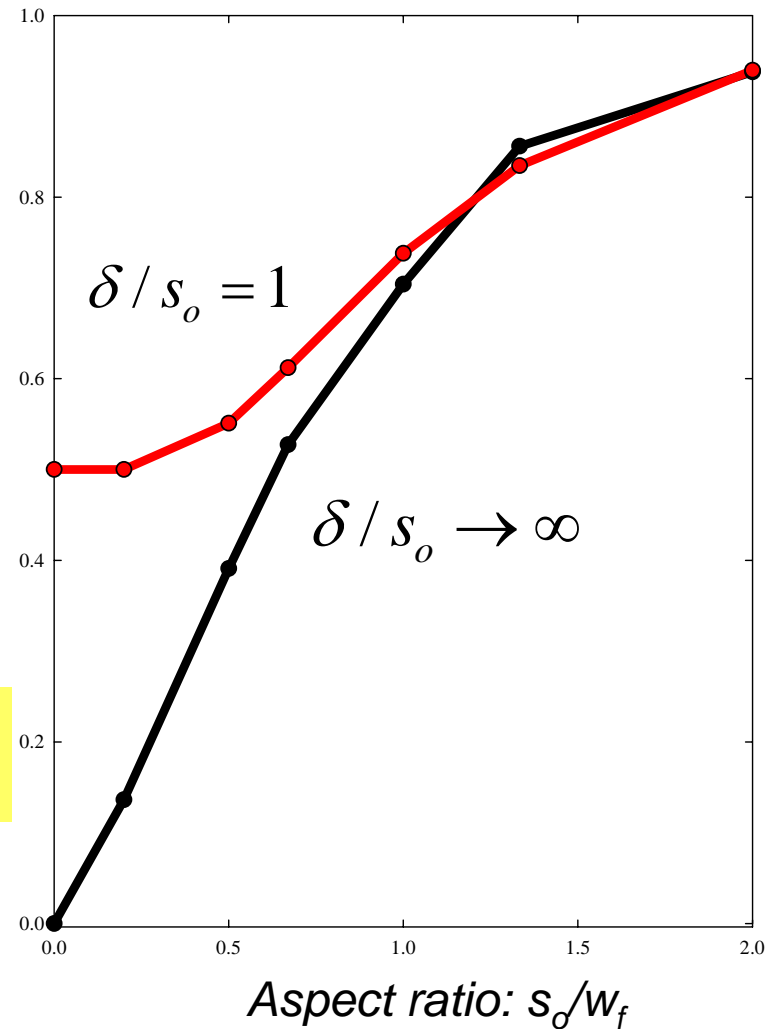
$$\text{dissolution rate } u \propto \frac{\partial \psi}{\partial n}$$

Simulated Effect of Aspect Ratio

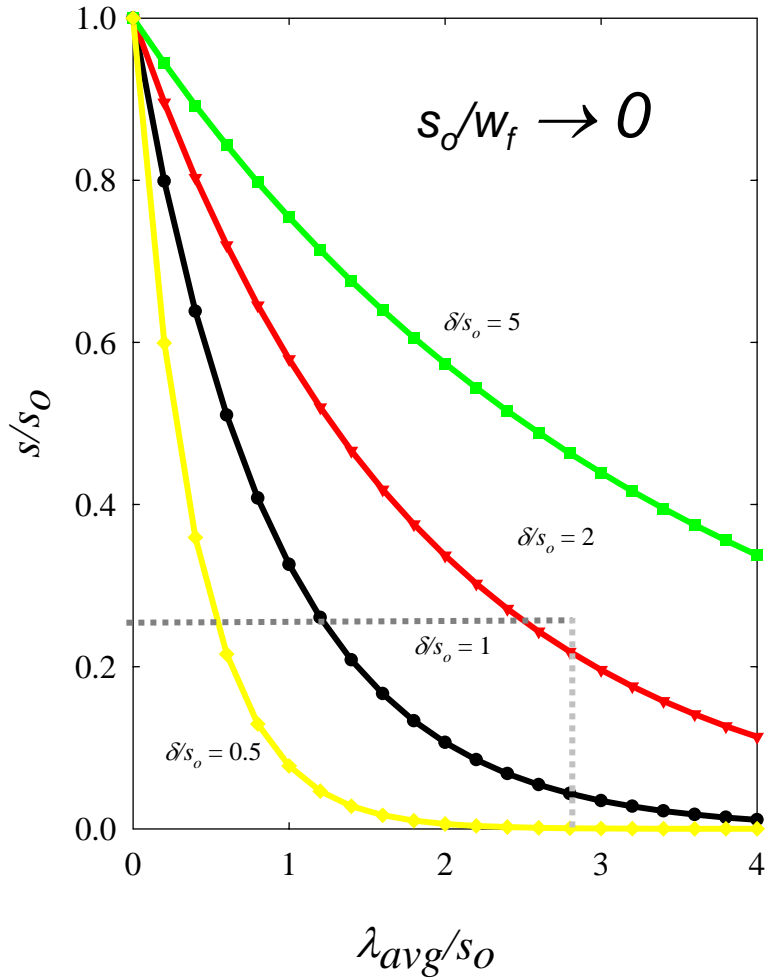
Small aspect ratios are most difficult to planarize

Upper limit in step-size reduction over average amount of material removed

$\Delta s_{\max} / \lambda_{\text{avg}}$



Simulated Reduction in Step Size of Small-Aspect Ratio Trench



Assume:

$s_0 < 250$ nm from ECD

And we require from polishing :

$\lambda_{avg} = 700$ nm

final step height $s = 60$ nm

Design requires that:

$\delta < 500$ nm

Conventional electropolishing is infeasible

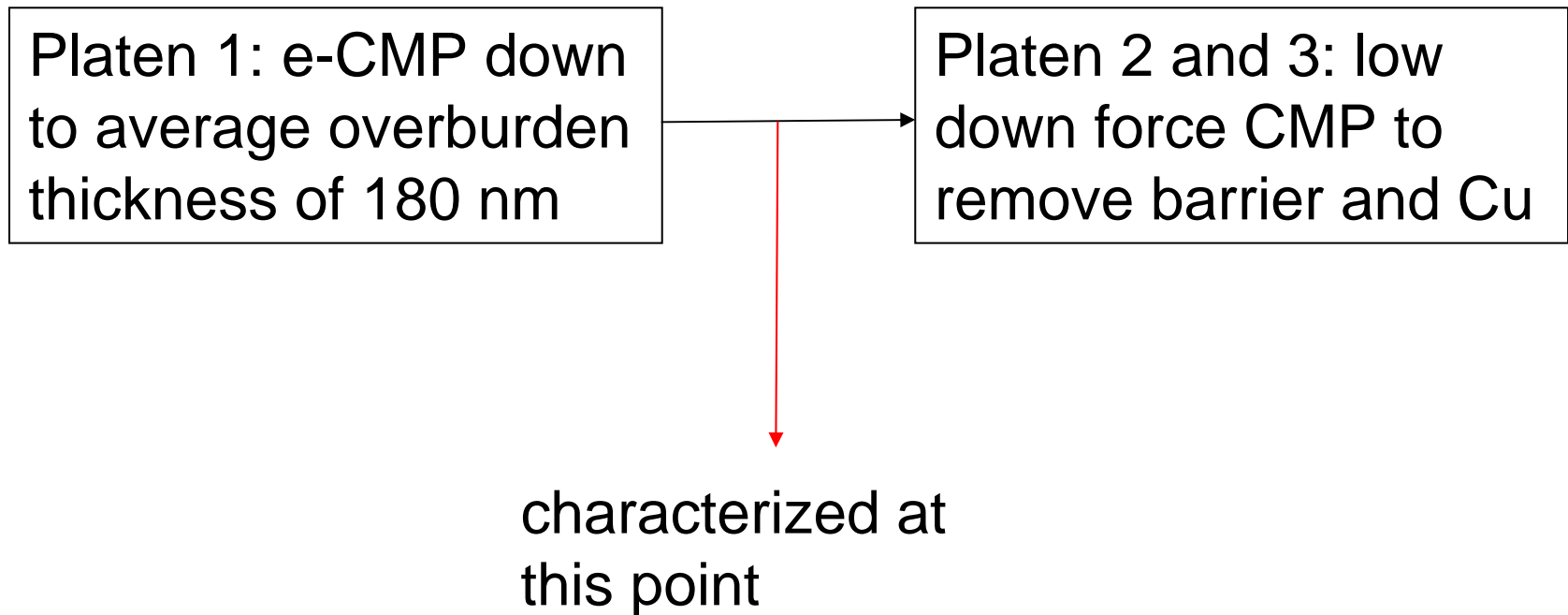
- For a wafer rotating at 100 rpm, using physical properties of phosphoric acid:

$$\delta = 27 \text{ microns}$$

- Further reductions require dramatic changes in chemistry, flow, design, or concept

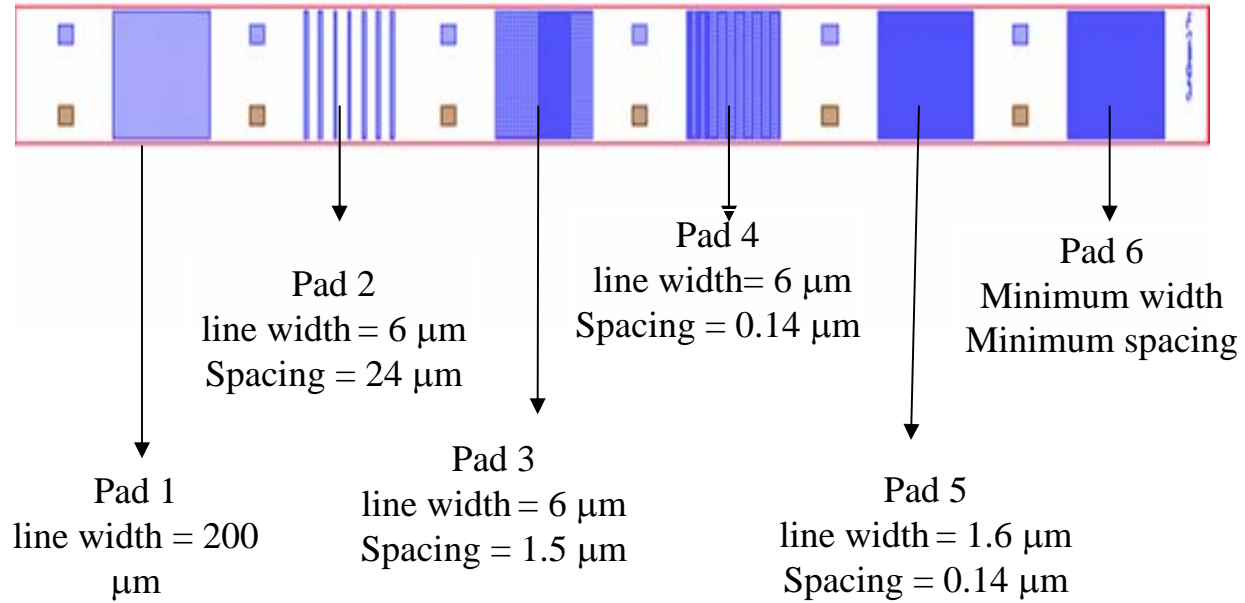
e-CMP is one example

Planarization Process*

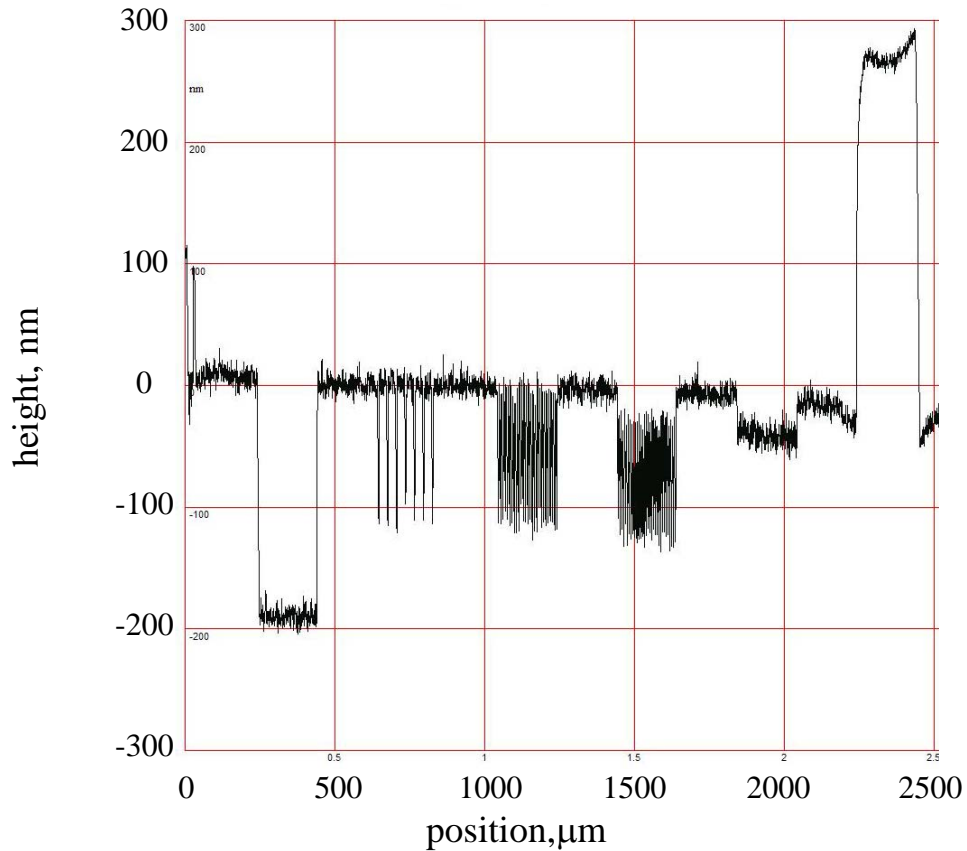


*Economikos et al., *IITC Conference Proceedings*, 233 (2004).

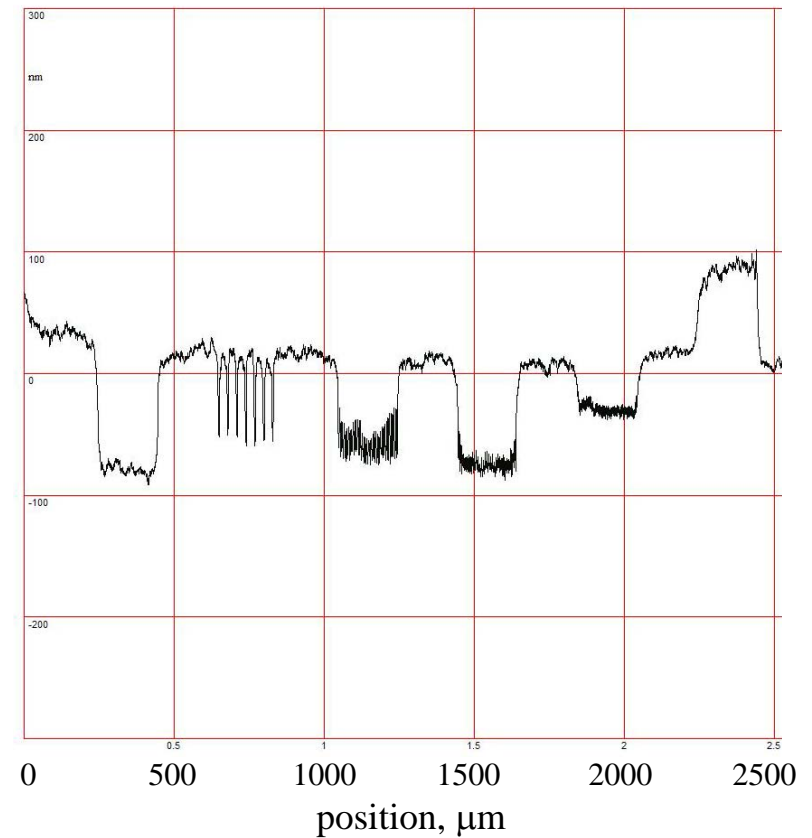
e-CMP Test Structure



As Deposited (350 nm overburden)

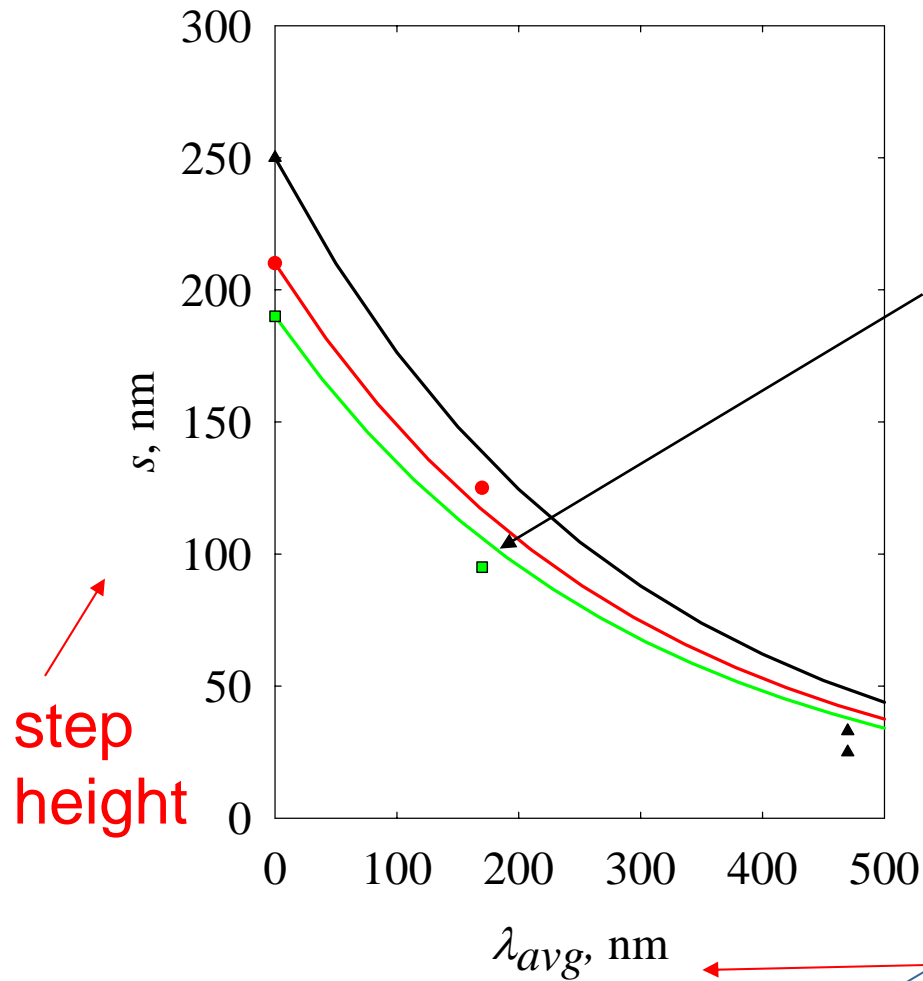


After Platen 1 (180 nm overburden)



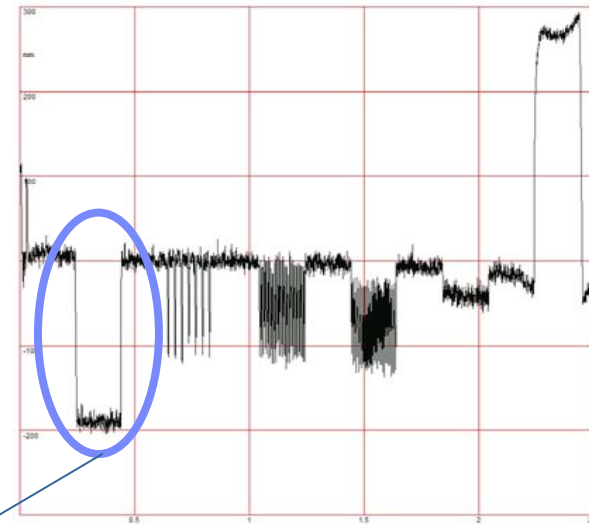
- Results obtained for 350 and 650 nm initial overburden thickness
- Also at wafer center and edge

Reduction in Step Height Above Dish



Lines are electropolishing simulations:

- Process is equivalent to $\delta = 315$ nm



Material removed

In theory, essentially same result for $s_0/w_f < 0.1$

Conclusions

- Small-Aspect ratio features are the most difficult to planarize
- Numerical simulations indicate the electropolishing is not likely to be effective
- Models based on Laplace's equation may describe e-CMP results
 - predictive over a limited range
 - Physical basis of length scale?

Acknowledgments

- Silvia Franz
- Wei-Tsu Tseng
- Steve Boettcher

- Panos Andricacos and Lili Deligianni for the opportunity to spend my sabbatical with IBM

Current Progress

- New Students
 - Kristin Shattuck
 - Paula Cojocaru – visiting scholar

- 1. Preliminary Design of Benchtop ECMP Device

- 2. Electrochemical Characterization
 - $\text{KPO}_3 - \text{H}_3\text{PO}_3$ / BTA System

Design of ECMP Apparatus

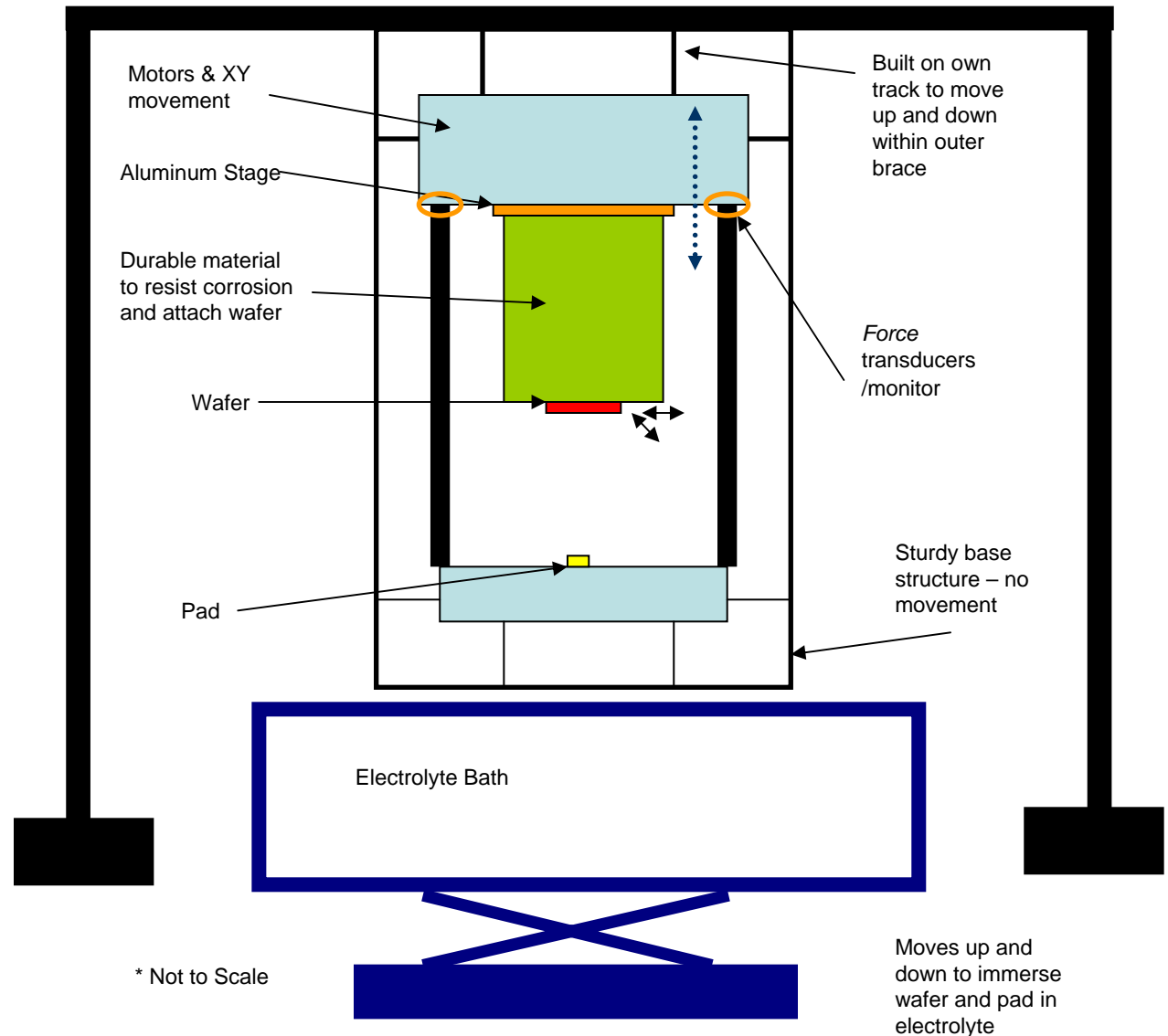
- Currently working with vendors to optimize specific design features

Wafer polishing motion

Planarization issues

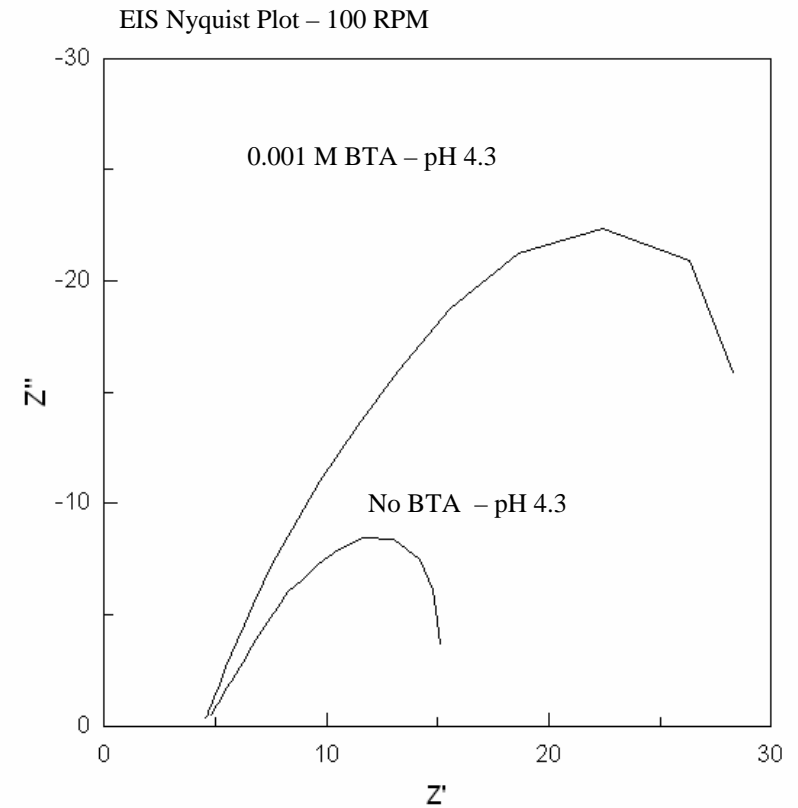
Controlling/
Monitoring force between wafer and pad

Wafer contacts



Electrochemical Characterization

- Experimental Parameters
 - pH
 - BTA concentration
 - Mass transfer
- Characterization
 - Electrochemical Impedance Spectroscopy (EIS)
 - Linear Sweep Voltametry (LSV)
 - Currently Investigating
 - Profilometry
 - 4 Point Probe



Planarization Capability

- Where:

I = current with
0.001 M
BTA

I_{no} = current with
No BTA

- Related to
dissolution rate
with and without a
polishing pad

