Study of Germanium Surface for Wet Cleaning Applications

Jungyup Kim Jim McVittie Krishna Saraswat Yoshio Nishi

STANFORD UNIVERSITY

May 18th 2006

NSF/SRC Engineering Research Center for Environmentally Benign Semiconductor Manufacturing

- 1. Background & Introduction
- 2. Etch Rate Study
- 3. Surface Roughness Study
- 4. Surface Passivation Study
- 5. Metal Removal Efficiency
- 6. Conclusions

1. Background & Introduction

- 2. Etch Rate Study
- 3. Surface Roughness Study
- 4. Surface Passivation Study
- 5. Metal Removal Efficiency
- 6. Conclusions

Background

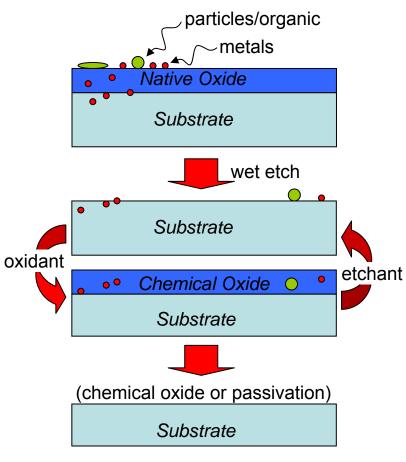
 Ge is gaining interest as a substrate for high mobility applications because of higher carrier mobility.
(2X electron & 4X hole mobility of Si)

(cm ² V ⁻¹ s ⁻¹)	Si	Ge
Electron	1450	3900
Hole	505	1800

Schäffler et al, Semiconductor Sci. Tech. (1997)

Therefore effective cleaning solution is required to fully utilize the high mobility properties of Ge in process integration.

Basics of Cleaning Process Development



Typical cleaning process flow

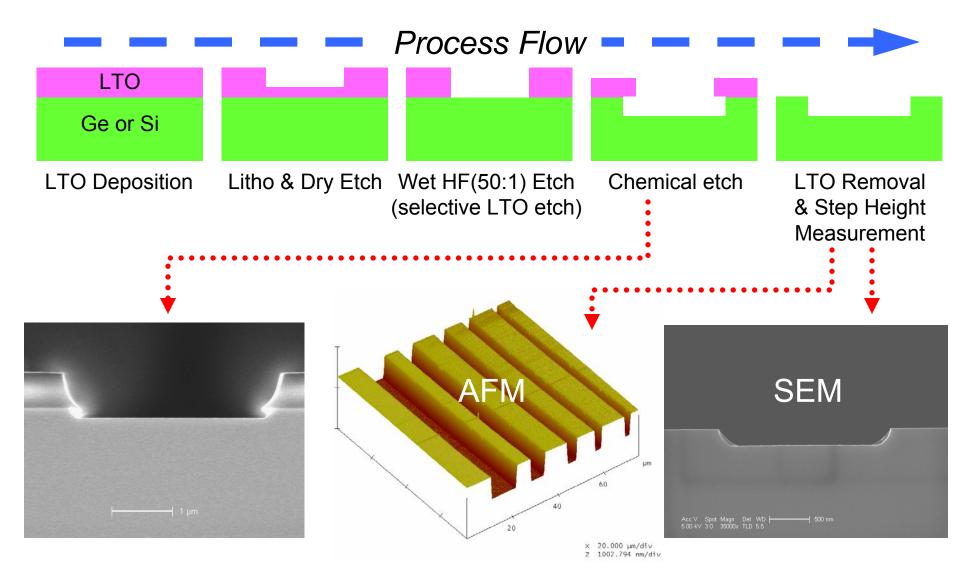
- Pre-requisite for Efficient Clean Process
- 1. Minimal Consumption of the Substrate
 - Study of etch rates of Ge in various chemicals
- 2. Minimal Increase in Surface Roughness
 - Effect of various chemicals on surface roughness with AFM
- 3. Efficient Metal Removal Efficiency
 - Intentional contamination and metal removal efficiency using ICP-MS
- 4. Efficient Passivation Characteristics- Timed passivation study with XPS
- Evaluation of the pre-requisites will allow us to set-up guidelines to develop an integrated cleaning process for Ge.

1. Background & Introduction

2. Etch Rate Study

- 3. Surface Roughness Study
- 4. Surface Passivation Study
- 5. Metal Removal Efficiency
- 6. Conclusions

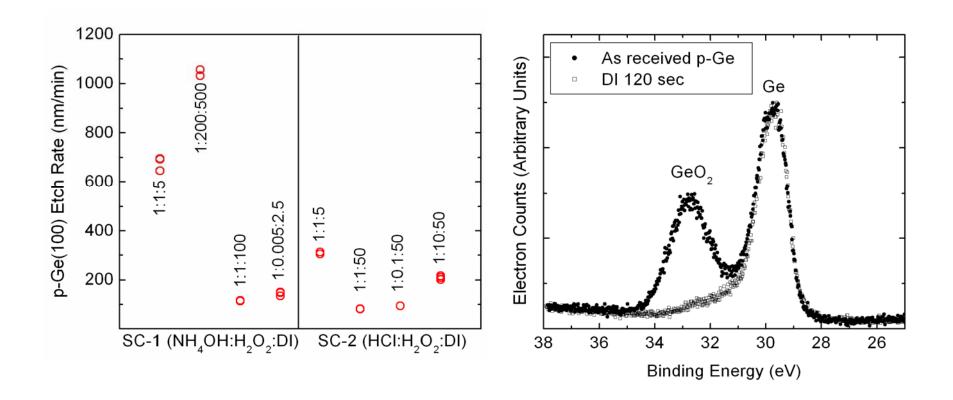
Etch Rate Study



Templates were used to measure etch rates by AFM and SEM

NSF/SRC Engineering Research Center for Environmentally Benign Semiconductor Manufacturing

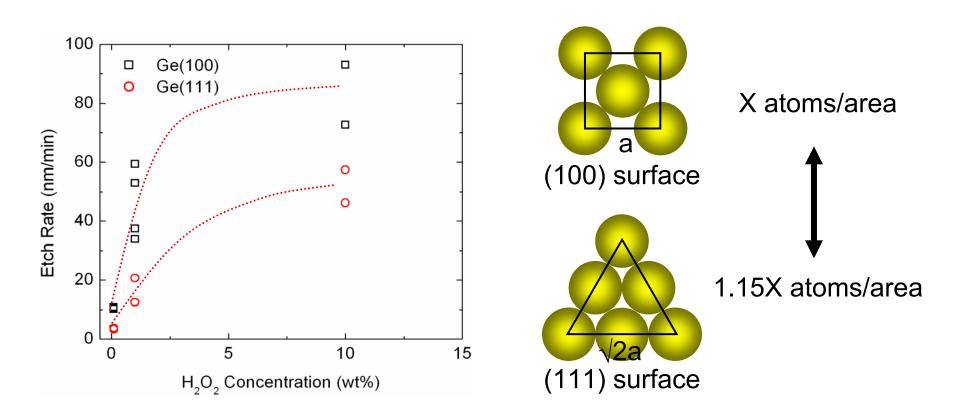
Etch Rate Study



High etch rates are observed for room temp SC-1 & SC-2. This is due to the fact that GeO₂ is soluble in DI. XPS study reveals that GeO₂ dissolves in DI water.

8

Etch Rate Study



High etch rates are observed for H₂O₂ and saturates at high conc. H₂O₂ oxidizes the surface and DI water etches the formed oxide. For minimum consumption and etch controllability, very dilute H₂O₂ need to be studied. Ge(111) has lower etch rates than Ge(100).

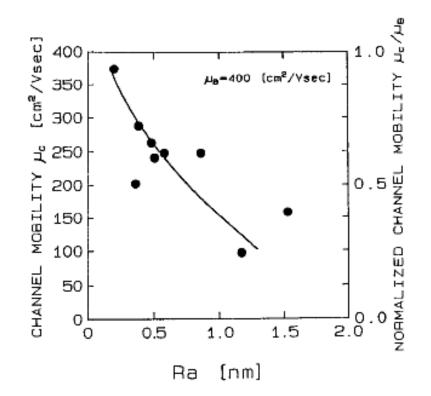
Etch Rate Study - Conclusions

- 1. Ge has very high etch rates in oxidizing aqueous cleaning solutions (700 nm/min in 1:1:5 SC-1 @room temp)
- 2. Ge has negligible etch rates in DI water. (0.007nm/min)
- 3. GeO_2 is soluble in DI water.
- 4. Ge has concentration & crystallographic dependent high etch rates in H_2O_2 solutions.

- 1. Background & Introduction
- 2. Etch Rate Study
- 3. Surface Roughness Study
- 4. Surface Passivation Study
- 5. Metal Removal Efficiency
- 6. Conclusions

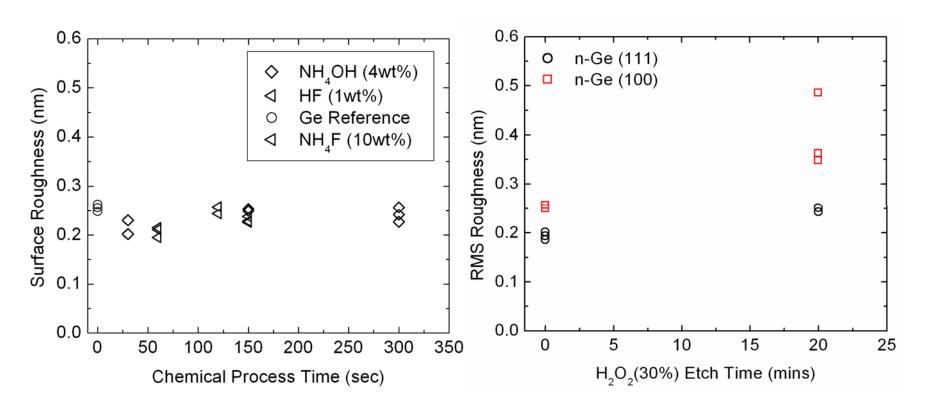
Surface Roughness

- Surface Roughness Effects
- 1. Degradation of mobility
- 2. Q_{bd} degradation
- 3. Metrology difficulties (unable to distinguish between particles and surface roughness)



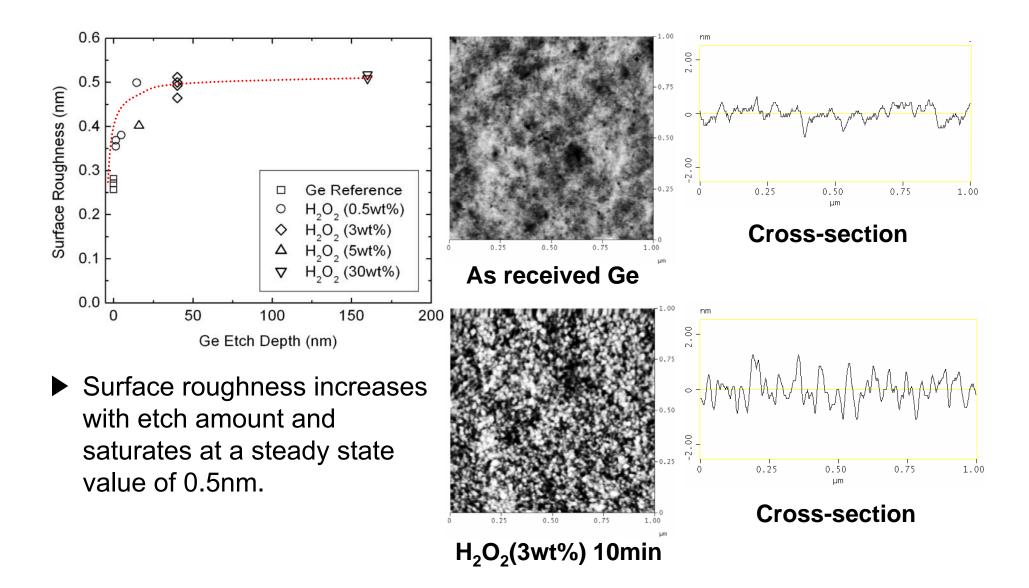
Degradation of Mobility due to surface roughness (Ohmi et al, *IEEE Trans. on Electron. Devices* (1992))

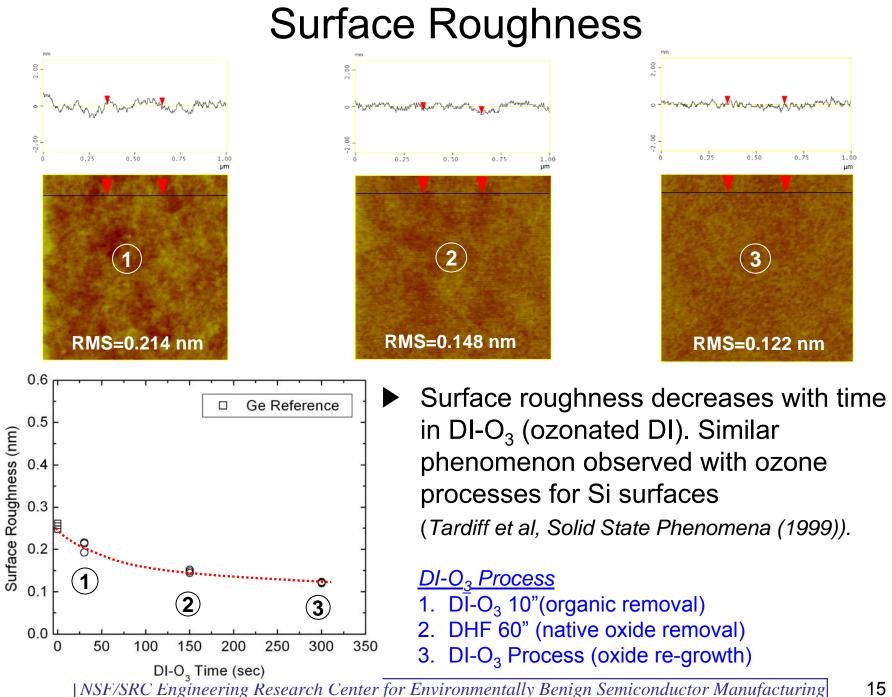
Surface Roughness



- Surface roughness does not increase in non-etching solutions (NH₄F, HF, NH₄OH and DI water). Surface roughness can be minimized by minimizing the etch amount.
- Surface roughness is direction dependent. Ge(111) has lower surface roughness than Ge(100)

Surface Roughness



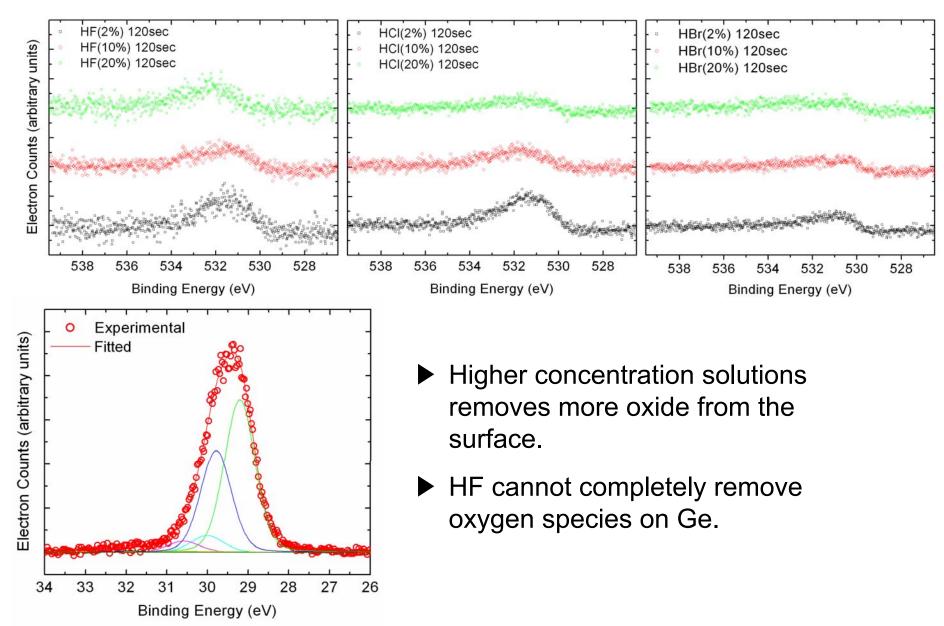


Surface Roughness - Conclusions

- 1. Surface roughness increases with etching amount in H_2O_2 and saturates at RMS=0.5nm.
- 2. Surface roughness increases with etching amount and does not change in non-etching solutions such as HF, NH_4F , NH_4OH and DI (0.007 nm/min).
- 3. Surface roughness is less for Ge(111) crystallographic direction.
- 4. Surface roughness decreases with $DI-O_3$ process time and reaches that of prime Si wafers (RMS=0.12nm).

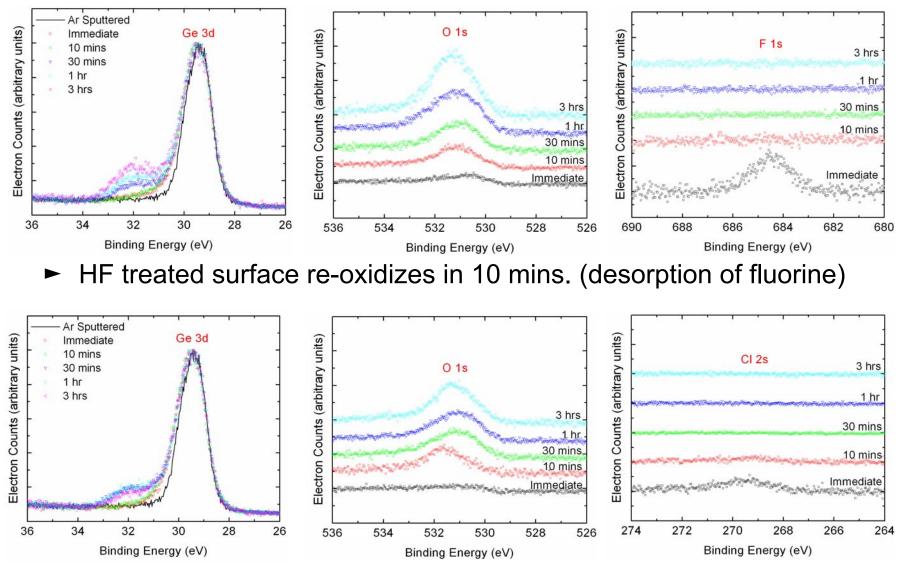
- 1. Background & Introduction
- 2. Etch Rate Study
- 3. Surface Roughness Study
- 4. Surface Passivation Study
- 5. Metal Removal Efficiency
- 6. Conclusions

Surface Passivation



NSF/SRC Engineering Research Center for Environmentally Benign Semiconductor Manufacturing 18

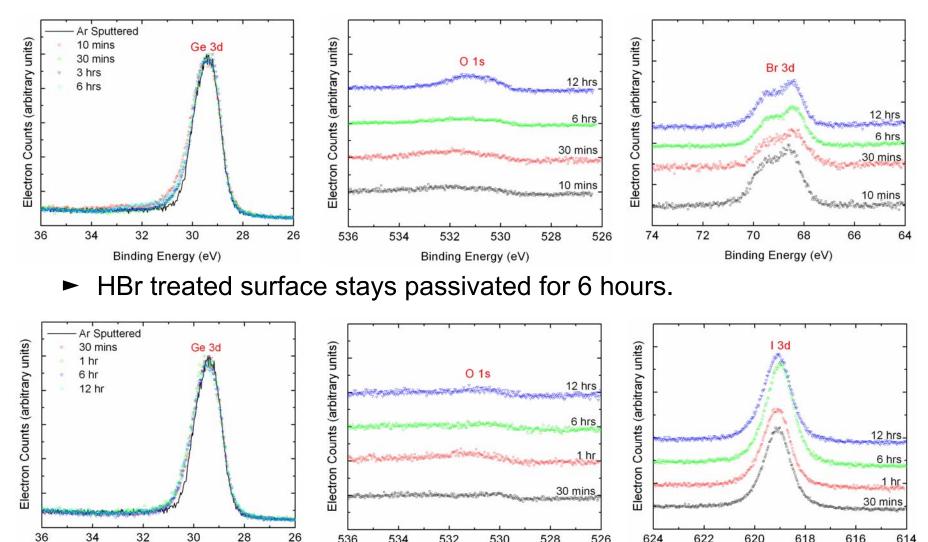
Ambient Stability of HF & HCI Treated Surface



► HCI treated surface re-oxidizes in 10 mins. (desorption of chlorine)

NSF/SRC Engineering Research Center for Environmentally Benign Semiconductor Manufacturing 19

Ambient Stability of HBr & HI Treated Surface



HI treated surface stays passivated for 12 hours.

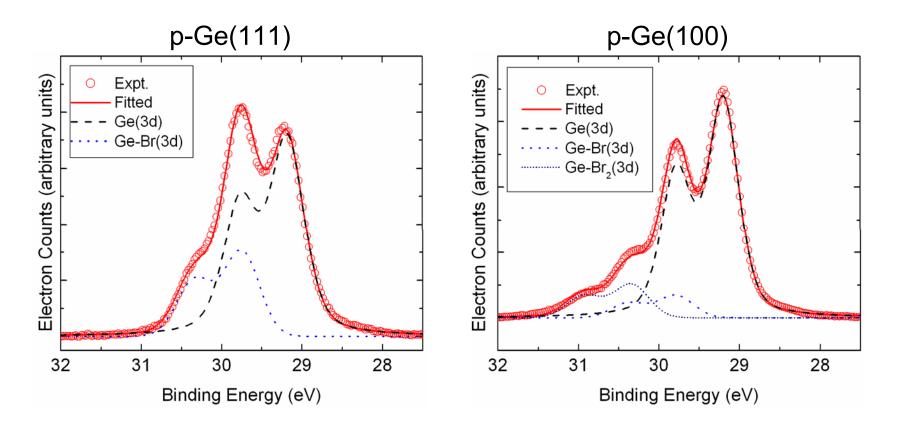
Binding Energy (eV)

NSF/SRC Engineering Research Center for Environmentally Benign Semiconductor Manufacturing

Binding Energy (eV)

Binding Energy (eV)

Synchrotron XPS Study of HBr Treated Surfaces



Ge surfaces are bonded to either 1 or 2 Br atoms depending on the crystallographic direction. Quantitative calculation show that the surface dangling bonds are passivated by the Br atoms.

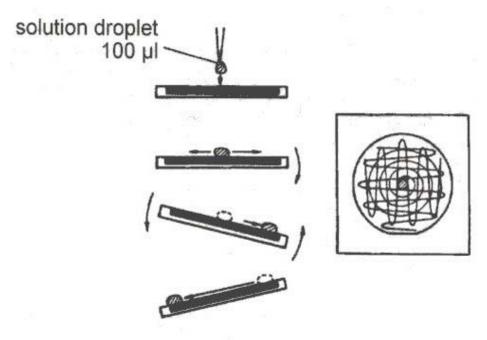
```
(Beam Energy = 80eV)
```

Surface Passivation – Conclusions

- 1. Higher concentration solution of HCL, HBr and HI remove more oxide from the surface. Concentrated HF leaves behind a sub-oxide layer.
- 2. HBr & HI passivation of Ge(100) is stable up to 6 hrs & 12 hrs in ambient, respectively .
- 3. Synchrotron XPS results show that Br passivates the dangling bonds of the Ge(100) and Ge(111) surfaces,

- 1. Background & Introduction
- 2. Etch Rate Study
- 3. Surface Roughness Study
- 4. Surface Passivation Study
- 5. Metal Removal Efficiency
- 6. Conclusions

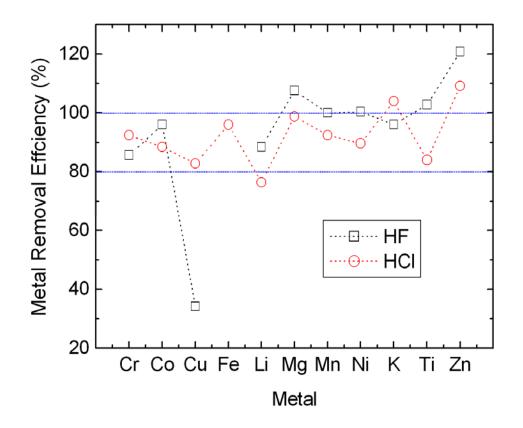
Metal Removal Efficiency – Method



(Ultraclean surface processing of silicon wafers : secrets of VLSI manufacturing / Takeshi Hattori (ed.) (1998))

Clean Ge wafers were intentionally contaminated and contaminant containing native oxide layer is stripped by a cleaning solution droplet allowing one to measure the metal removal efficiency of a particular solution.

Metal Removal Efficiency – Results & Conclusion



Both HF and HCI metal removal efficiencies of greater than 80%. Both HF and HCI is efficient in removing metals from the surface but previous XPS results show that HCI can completely remove the sub-oxide and is relatively more stable in ambient than HF.

- 1. Background & Introduction
- 2. Etch Rate Study
- 3. Surface Roughness Study
- 4. Metal Removal Efficiency
- 5. Surface Passivation Study

6. Coclusions

Conclusions

- 1. Ge has crystallographically dependent high etch rates in oxidizing (H_2O_2) aqueous cleaning solutions.
- 2. RMS surface roughness increases with etching amount in H_2O_2 and saturates at 0.5nm.
- 3. Surface smoothening effect is observed in $DI-O_3$ solutions.
- 4. HBr & HI is effective in passivating Ge(100) surface up to6 hrs & 12 hrs in ambient, respectively.
- 5. HCl is a promising candidate in effectively removing metal contamination from Ge surface.

Future Works

- 1. Using the results from the individual experiments; optimization of a fully integrated process looking at the substrate consumption, metal removal efficiency, surface roughness and surface passivation.
- 2. Effect of cleaning on the electrical properties of the gate stack on Ge surface.