NSF Nanoscale Science and Engineering Center for High-rate Nanomanufacturing (CHN)

### Rethinking Manufacturing; Directed Assembly Based Nanomanufacturing and the Role of CMP

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Center for High-rate Nanomanufacturing



🕅 Northeastern University



**Director: Ahmed Busnaina, NEU** 

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www.nano.neu.edu

### Outline

- Introduction to Nanomanufacturing
- Nanoscale Manufacturing Processes using Directed Assembly (Nanomaterials based Manufacturing)
- The Role of CMP in Directed Assembly
- > Applications
- Summary



#### The Vision; Rethinking Manufacturing

Today, a fabrication facility that manufacture nanoscale devices (such as consumer electronics) costs \$5-10 billion US dollars. This high cost entry barrier completely shuts out small, medium and many other large corporations.

> **Present:** Microelectronics Factory: \$5B-\$10B\* **Future Goal:** Nanomanufacturing Factory: \$25M-\$50M

\*Sources: \* Global foundries, press releases from TSMC, Intel and Samsung; Viking Waters, Inc.



Manufacturing is the process of adding or removing materials by means of a large-scale industrial operation, which can take place at macro, micro or nano scales.

Macro-manufacturing involves cutting, coating, shaping, welding and assembly of various parts.

Current Micro and nanoscale manufacturing involves deposition (thin film using chemical or physical processes), etching, polishing, assembly, packaging, and wire bonding.

Directed assembly based Nanomanufacturing involves adding materials selectively such that no material removal is needed, thereby both reducing waste and the number of required processes.



Present: Microelectronics Factory: \$5B-\$10B >>>> Future: Nanomanufacturing Factory: \$25-50M



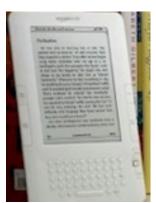


### Is this possible?

# Could nanoscale manufacturing cost be 100 times lower than today's cost?

> We are already seeing early signs of shifting manufacturing of devices and other products from vacuum-based process. For example, some display applications, already commercialized, using ink-jet or other printing of circuit patterns.









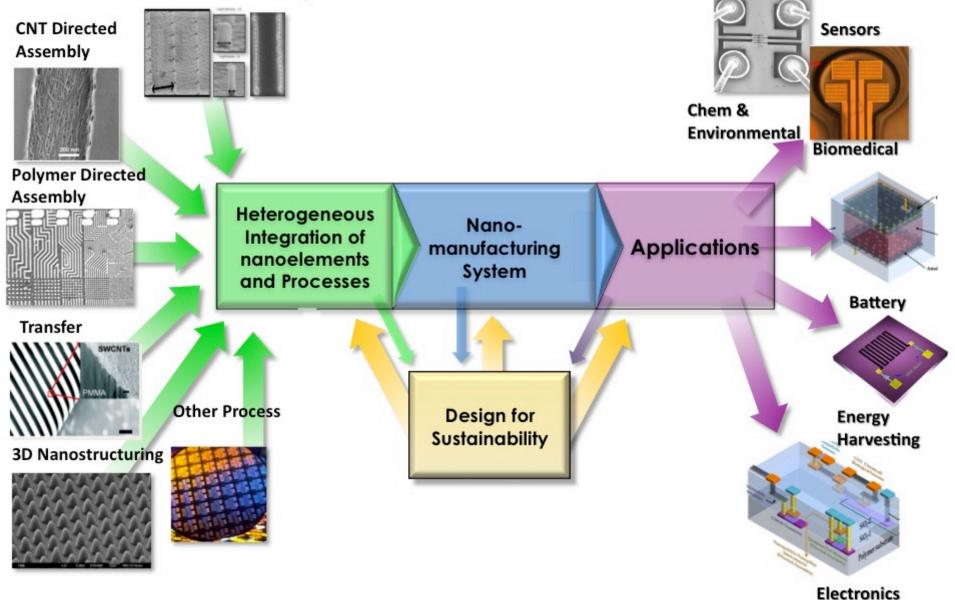
However, these present technologies are all top down and cannot be scaled down to make nanostructures.

Directed assembly (of Carbon nanotubes, Nanoparticles, polyemers and other nanoelemets) have already been shown to be scalable by the CHN and other researchers.

In addition, directed assembly based nanomanufacturing processes are operated at room temperature and pressure (no vacuum or high temperature), which will provide a significant cost reduction in equipment, energy, and maintenance costs



#### NP Directed Assembly



#### Nanomanufacturing Platform

## A complimentary set of tools to selectively add and remove material at the nanoscale

Heterogeneous integration of different Nanoelements and processes						
CNT Directed AssemblyNPs Directed Directed AssemblyPolymer Directed Directed AssemblyTransferTop Down Processes						
Present: Microelectronics Factory: \$5B-\$10B* >>>> Future: Nano Factory: \$25M-\$50M						
Manufacturing Microelectronics: Vacuum, high temp. >> Nanomanufacturing: Atmospheric, room temp.						

#### Preliminary Estimate of Manufacturing Cost Comparison

Cost Factor	Equipment	Raw Wafer	Water/Utility	Chemical/Gas /	Maintenance	Labor	Total
				Consumable			
Platform							
Microelectronics							
(300mm)	1000	140	200	300	220	90	\$1,950
Nanomanufacturing		140	20	30	4	30	\$ <b>240</b>
Nanomanuracturing	10	140	20		4	30	\$240
% Cost Reduction	99%	0%	90%	90%	98%	66%	88%

## The Vision; Rethinking Manufacturing

- Manufacture nanoscale systems and devices at a fraction (one hundredth) of today's cost.
- Unleash a wave of creativity by making nano scale manufacturing accessible and affordable in the same way as the advent of PC technology did to the computing industry.
- Increasing access will lead to increase in innovation and consequently the creation of entirely new industries.

Computing revolution in 1980s					
Mainframe PC					
Cost: \$0.5M to 5M**	Cost: \$1200 to \$5000 (1981)				
Users: thousands	Users: Billions				
Industry: IBM, Unisys, DEC, Burroughs Industry: Dell, HP, Intel, Microsoft					
Jobs: Thousands Jobs: Millions					



### **Team Strength and Capability**

**NEU:** Directed assembly, MEMS, fabrication, nanoscale contamination control



**UML:** High volume polymer processing and assembly

#### Semiconductor & MEMs fab

- 7,000 ft<sup>2</sup> class 10 and 100 cleanrooms
- 6 inch completer wafer fab, nanolithography capabilities

#### **UNH:** Synthesis, self-assembly





Center for High-Rate Nanomanufacturing

A unique partnership



Plastics processing labs

- 20,000 ft<sup>2</sup> +
- Compounding and forming equipment

Fully-equipped synthetic labs • 10,000 ft<sup>2</sup> +

Institution	Faculty	Post-docs	Graduate	Undergrad.	Total
NEU	14	8	19	14	50
UML	18	6	35	16	75
UNH	6	5	12	13	36
MSU	1	1	1	0	3
TOTAL	39	20	67	43	169

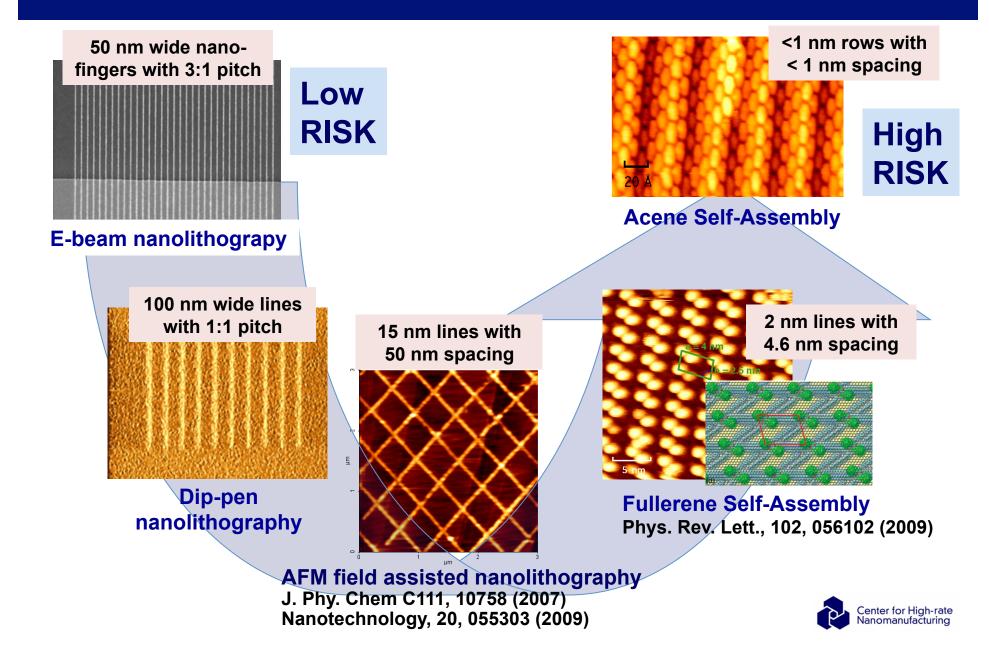


### **Directed Assembly Processes**

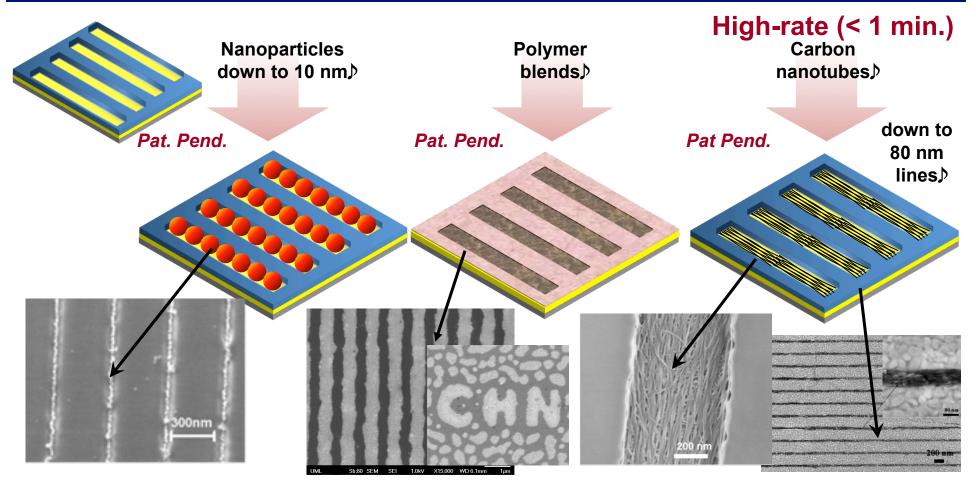
Process	Nanoelement property	Nanoelements
Electrophoretic Asssembly	Charge	Nanoparticles, CNTs, polymers
Chemical Functionalization	Functionalization	Nanoparticles, CNTs, polymers
Electrophoretic and chemical functionalization	Charge and surface functionalization	Nanoparticles, CNTs, polymers
Dielectrophoretic	Dielectric constant	Nanoparticles, CNTs, polymers
Convective	Surface Functionalization	Nanoparticles, CNTs
Convective interfacial	Surface Functionalization and surface tension	Nanoparticles, CNTs



### **Making Templates**



### Nanotrench Template Directed Assembly Using Electrophoresis or Chemical Functionalization



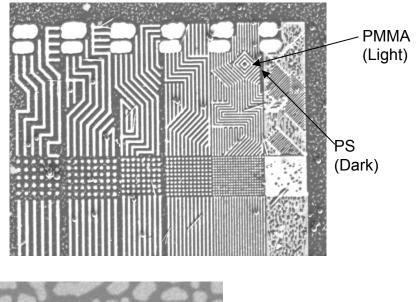
Xiong, X, Busnaina, A, et. Al., *Appl. Phys. Lett.* 2007.

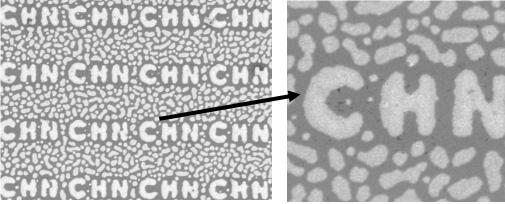
Wei, M. Liang F., Lee, J. Somu, S., Xiong, X, Barry, C., Busnaina, A., Mead, J, *Advanced Materials, 2009*. Xiong, X, Jaberabsari, L, Hahm, M G, Busnaina, A, and Jung, Y, J, *Small, 2007*. Makaram, P, Somu, S, Xiong, X, Busnaina, A, Jung, Y J, and McGruer, N, *Appl. Phys. Lett., 2007*.

### Last Year: Multi-scale Patterned Polymer Blends

- Chemically functionalized templates assemble PS/ PMMA polymer blends into non-uniform geometries.
- Polymer domains were patterned from 300 nm down to 100 nm on the same template.

#### PS/PMMA (50/50 ratio)



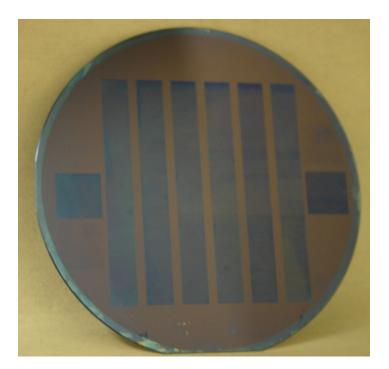


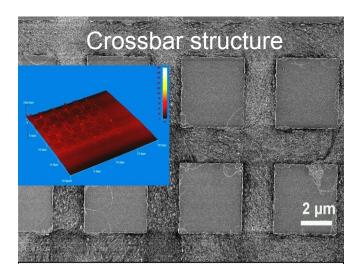
Chiota et al., Small, 2009 Dec;5(24):2788-91



### Template Guided Directed Assembly of SWNTs

 Assembly of SWNTs over large areas on templates with different surface energies



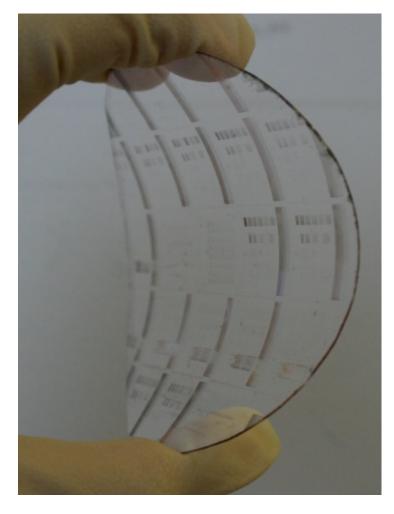


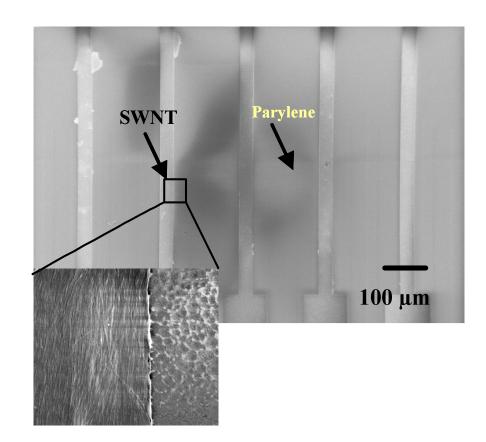
Xiong, X, Jaberabsari, L, Hahm, M G, Busnaina, A, and Jung, Y, J, *Small*, **3** (12) 2006 (2007) Jaber-Ansari, L, Hahm, M G, Somu, S, Echegoyen Sanz, Y, Busnaina, A, and Jung, Y J, *J. Am. Chem. Soc., 131 (2), pp 804 (2009)* Jaberasani, L., Somu, S. Hahm, M G, Busnaina, A, and Jung, Y J, *Appl. Phys. A., 5194 (2009)* 



### **Template Guided Directed Assembly**

- Large scale assembly on polymer substrates
  - Enables assembly of lines over large areas (i.e., centimeters)



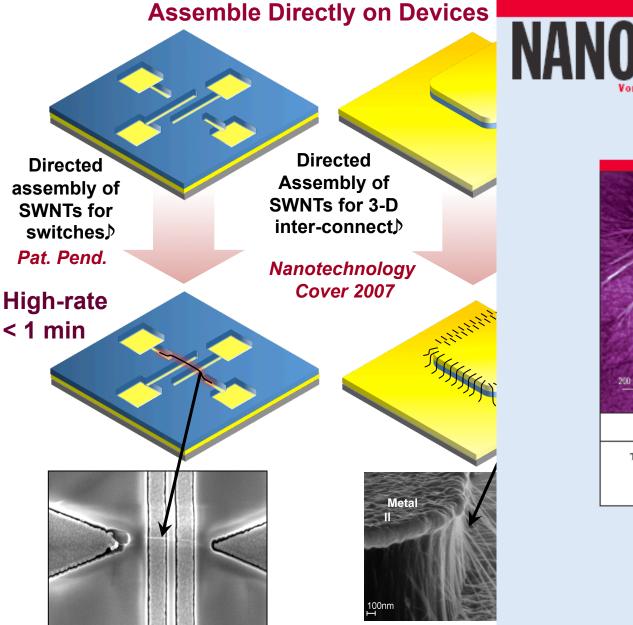


Patterned, aligned CNTs on a parylene, polycarbonate or polystyrene wafers

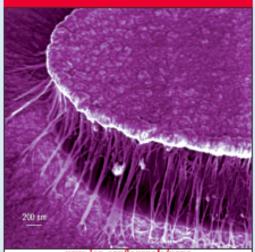




#### **Template-free Dielectrophoretic Directed Assembly**



NANOTECHNOLOGY VOLUME 18 NUMBER 39 3 OCTOBER 2007



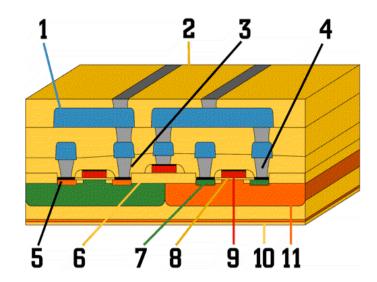
www.iop.org/journals/nano

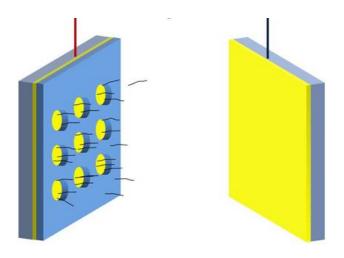
Featured article: Three-dimensional assembly of single-walled carbon nanotube interconnects using dielectrophoresis P Makaram, S Selwarata, XXiong, C-L Chen, A Biomaina, N Khanduja and M R Doknaci

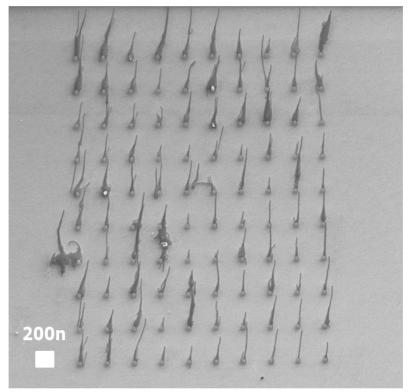
IOP Publishing

#### **CMOS** Technology Interconnects

Room temperature 3D assembly of CNTs for CMOS interconnects







3-D Assembly of SWNT in CMOS vias over a wafer level



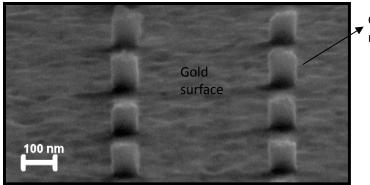
#### **Developed Method of Fabricating 3-D Nanostructures**

- Fast
- ➤ Scalable
- Low cost
- Room-temperature and

#### pressure

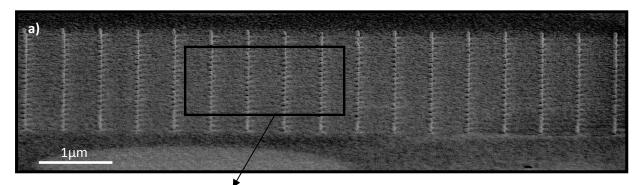
- Chemical- free
- Environmentally friendly
- No need a seed layer
- Material independent
- Hybrid nanostructures

#### **Interconnects and Nanorods**



Gold nanorods

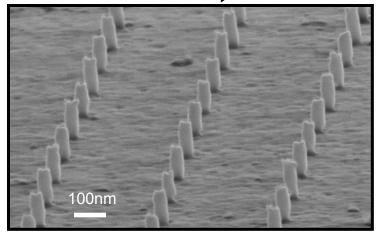
> A high angle SEM of fabricated 100nm nanorods. 12Vpp was applied to the 5nm gold nanoparticles at the frequency of 10 kHz.

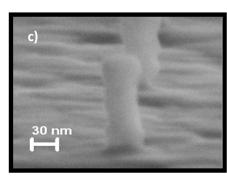


a) SEM image of50nm nanorods over10µ x 10µ area.

b) A magnified image of the Nanorods array.

c) High magnification image of a single rod.

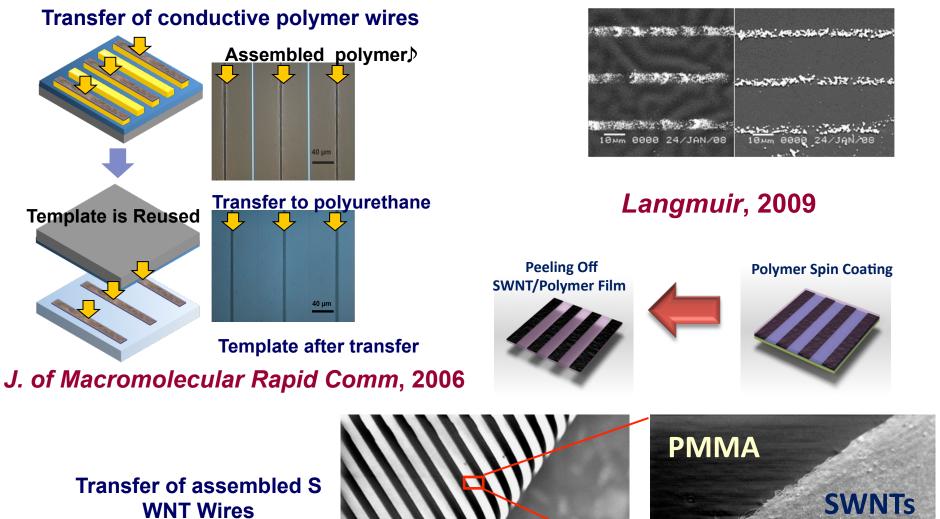




Yilmaz, Busnaina, et. Al, *IEEE Trans on Nanotechnology 2010* 



### High-rate Transfer (< 1 min)



20 μm 20 μm Manomanufačturing

**Transfer of assembled nanoparticles** 



## The Role of CMP in Directed Assembly



Center for High-rate Nanomanufacturing

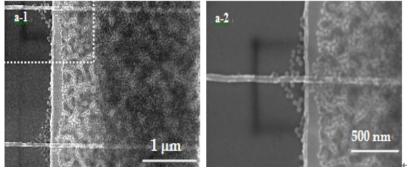
### Damascene Nanowire Templates; Motivation

#### **Previous Nanowire Template**

Metal nanowire



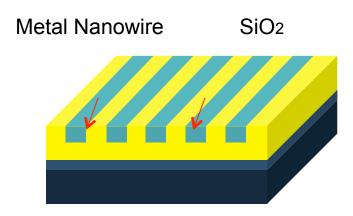
DC assembly, microwires attract most of the nanoparticles compared with nanowires



#### Disadvantage

- ≻ Huge potential drop → Nonuniform assembly.
- Nanowires burning.
- ➢ Poor adhesion → Nanowire peel off
- Non-uniform topography

#### **Damascene Nanowire Template**



#### **Advantages**

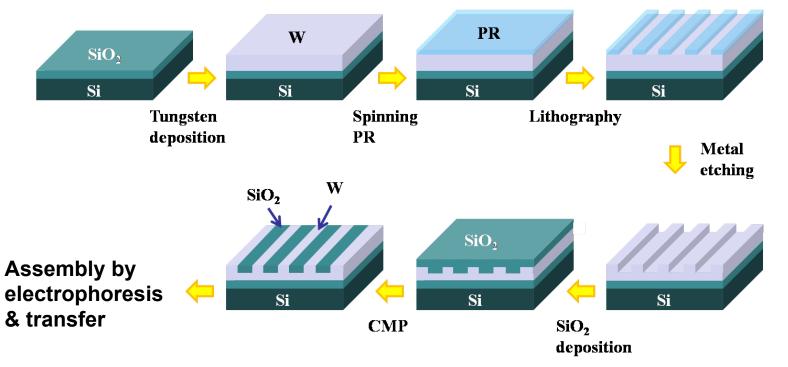
Equipotential on all the nano and microwires.

> High rate assembly with good uniformity.

- Strong adhesion
- Completely flat surface.



### Fabrication of the Damascene Nanowire Templates



: Schematics of damascene template fabrication

FI Thickness of W : 250 nm

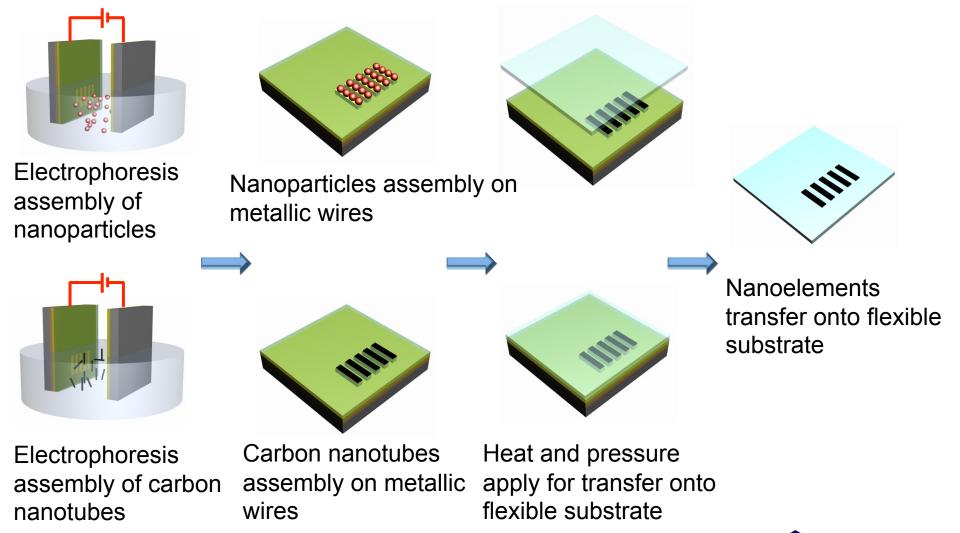
W

SiO<sub>2</sub>

FESEM image of damascene template cross section



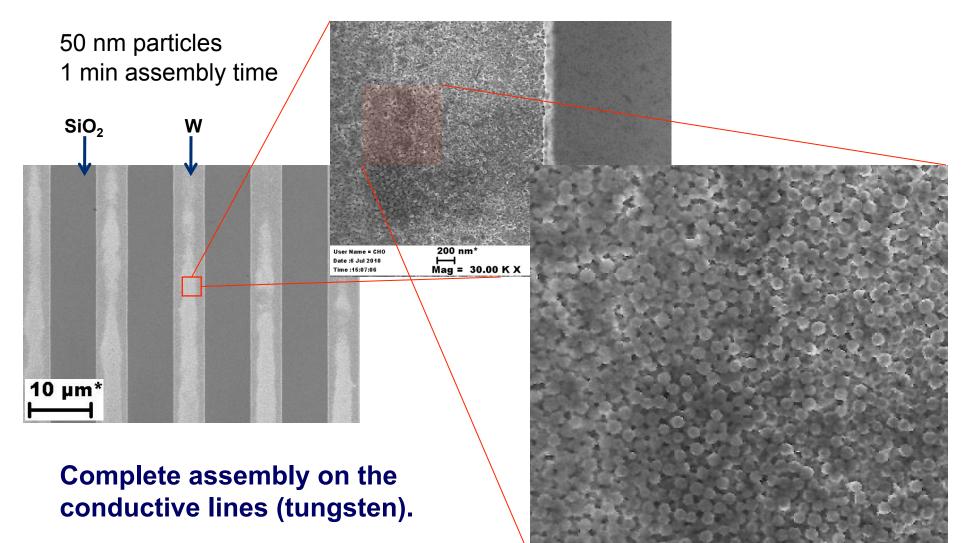
#### **Electrophoertic Assembly and Transfer**





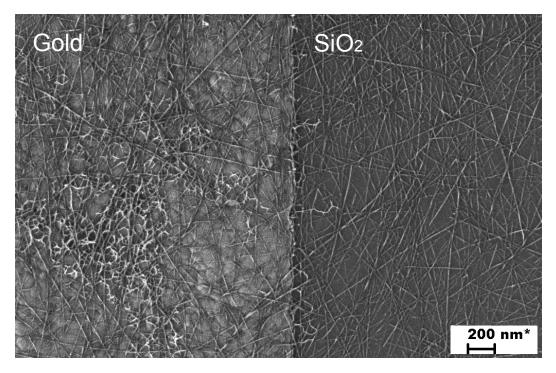
#### Nanoparticle Assembly by Electrophoresis on Damascene Templates

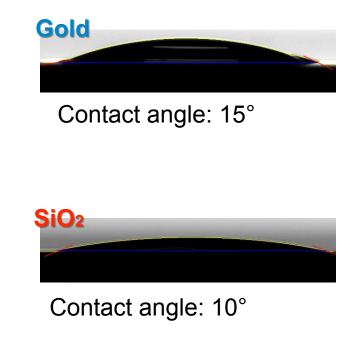
#### **Assembly condition**



### SWNT Assembly by Electrophoresis on Damascene Templates

## SWNTs were assembled on gold and even on SiO<sub>2</sub> lines

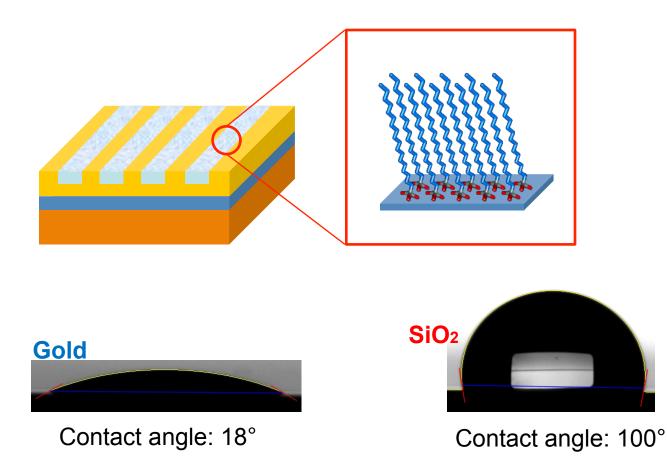




Self assembled monolayer (SAM) need to be used to change the surface energy.



#### Wetability Control using SAM



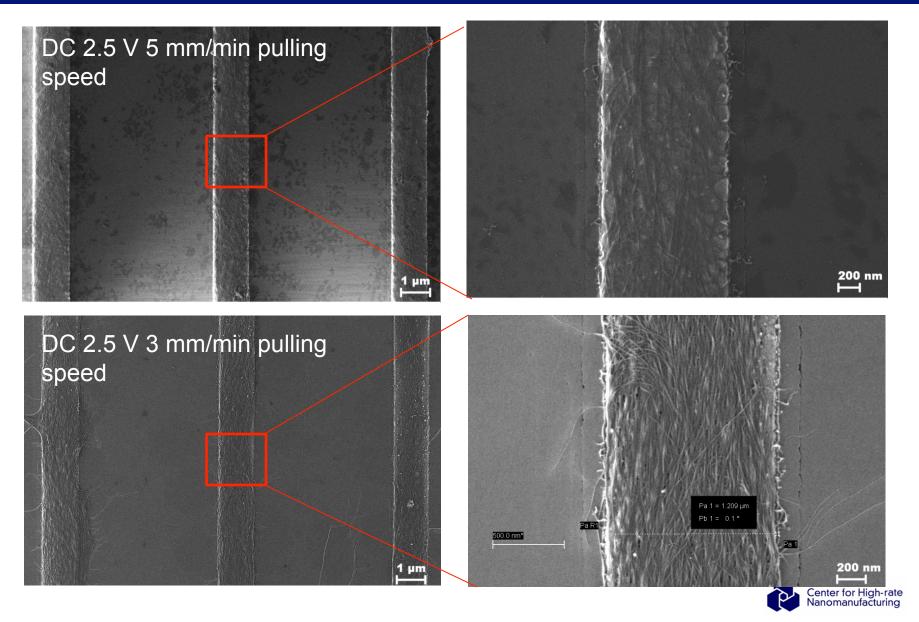
**OTS self assembled monolayer** 

⇒ SiO2 becomes hydrophobic

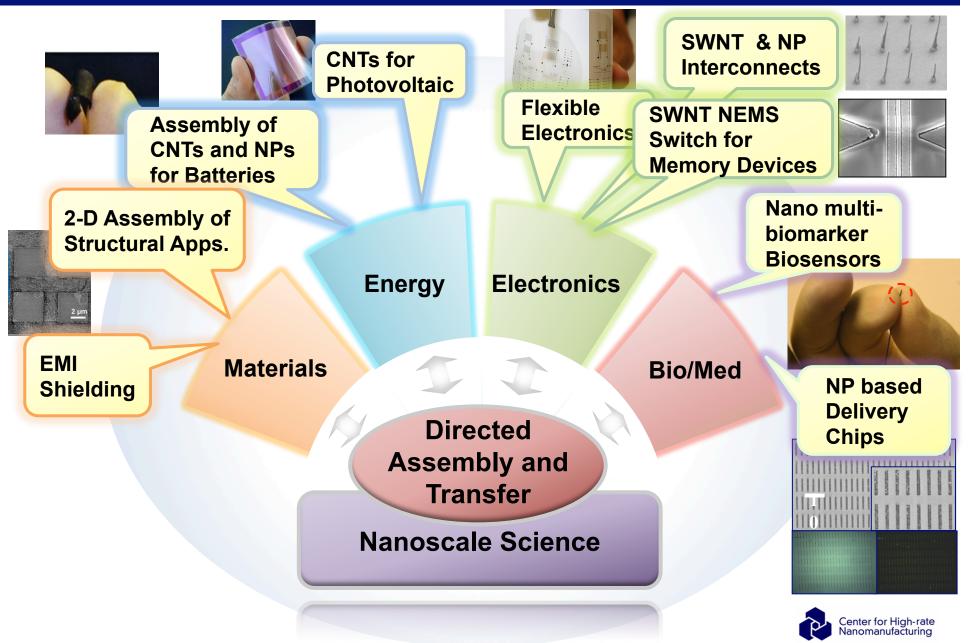
➡ Will prevent SWNTs from being assembled



#### SWNT Assembly on Functionalized on Damascene Templates



### Nanomanufacturing Applications Roadmap



## CHN Toolbox Connects Research to Applications

Templates	Nanoelements	Assembly Processes	Transfer Processes	Substrates	Applications
Microwires template	Nanoparticles	Electrophoretic 2-D and 3-D	Direct transfer (no functionalization)	Silicon	SWNT switch for memory devices
Nanowires templates	Carbon nanotubes (SWNTs and MWNTs)	Chemical Functionalization	Direct transfer with chemical functionalization	Polymer	Polymer-based Biosensors
Nanotrench template	Conductive polymers (PANi)	Electrophoretic and chemical functionalization	No transfer needed	Metal	Nanoparticle- based Biosensors
Template-free	Polymer blends	Dielectrophoretic 2-D and 3-D	Reel-to-reel transfer		SWNT Batteries
Damascene Template	Fullerenes	Convective	Switchable functionalization		Photovoltaics
	Acenes	Convective interfacial			SWNT Chem Sensors
	Graphene	Self assembly			EMI Shielding

### Process Flow for SWNT Chemical Sensors

Templates	Nanoelements	Assembly Processes	Transfer Processes	Substrates	Applications
Microwires template	Nanoparticles	Electrophoretic	Direct transfer (no functionalization)	Silicon	SWNT switch for memory devices
Nanowires templates	Carbon nanotubes (SWNTs and MWNTs)	Chemical Functionalization	Direct transfer with chemical functionalization	Polymer	Polymer-based Biosensors
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	Acenes	Convective interfacial			SWNT Chem Sensors
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### Process Flow for Nanoparticle-based Biosensors

Templates	Nanoelements	noelements Assembly Processes		Substrates	Applications
Microwires template	Nanoparticles	Electrophoretic	Direct transfer (no functionalization)	Silicon	SWNT switch for memory devices
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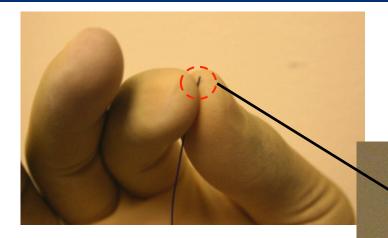


## CHN Directed Assembly Toolbox

Process	Speed	Scalability	Nanoelement property	Mechanism	Nanoelements
Electrophoretic Asssembly	Fast	Yes	Charge	Electrophoresis	Nanoparticles, CNTs, polymers
Chemical Functionalization	Fast/ slow	Yes	Functionalization	Chemistry	Nanoparticles, CNTs, polymers
Electrophoretic and chemical functionalization	Fast	Yes	Charge and surface functionalization	Electrophoresis and surface energy	Nanoparticles, CNTs, polymers
Dielectrophoretic	Fast	Yes/No	Dielectric constant	Dielectrophoresis	Nanoparticles, CNTs, polymers
Convective	Slow	No	Surface Functionalization	Convection	Nanoparticles, CNTs
Convective interfacial	Fast	Yes	Surface Functionalization and surface tension	Convection and interfacial force	Nanoparticles, CNTs

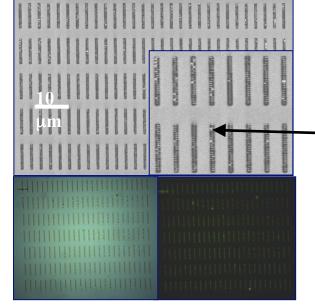


### In vivo Nano Biosensor

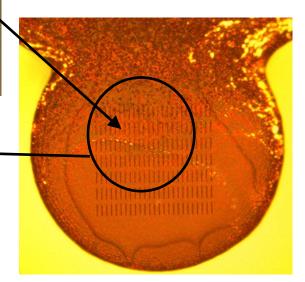


S. Siavoshi, C. Yilmaz, S. Somu, T. Musacchio, J. Upponi, V. Torchilin, and A. Busnaina, Langmuir, 27, (2011).

#### Image of the *in-vivo* biosensor (0.1 mm x 0.1 mm) after animal testing

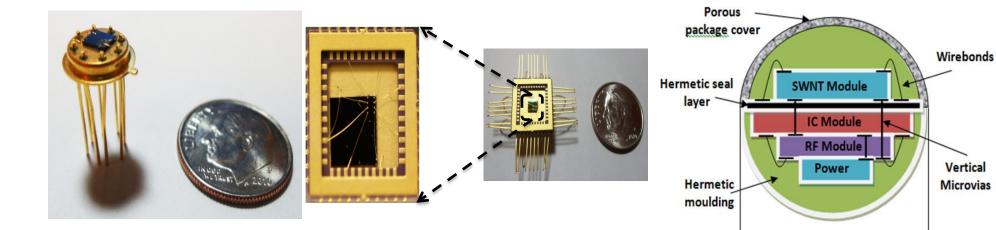


Incubated with human plasma spiked with CEA Detection limit: 15 pg/ml Current technology detection limit is 3000 pg/ml

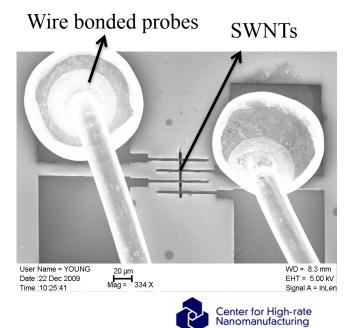




## **Chemical Sensors I**



- $\succ$  Sensor active area is less than 10 micron squared;
- Resistance based; Very high sensitivity ~ppm
- Fast, specific Multiple species Detection
- Working in harsh environment (~250C and 25 Kpsi) already tested for 600psi and 200C
- When combined with peripheral components, data storage and communication is possible
- Potential Robust platform for low cost, sensitive sensor With durability to withstand reservoir injection.



10-100µm

## **Chemical Sensors II**

Sensor active area is 2 micron

When combined with peripheral

components, data storage and position

Employs directed assembly

squared or less

Resistance based

CMOS integrated

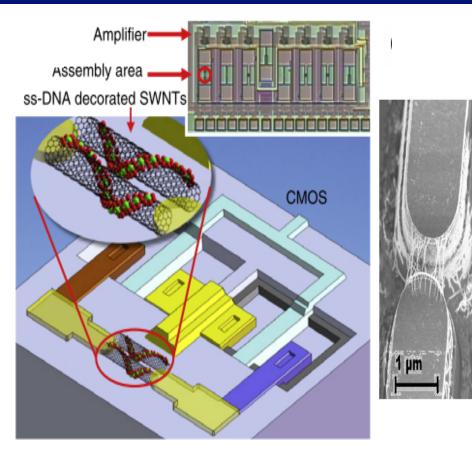
> Alcohol sensors

Fast detection

Highly portable

High sensitivity

identification is possible



#### Application

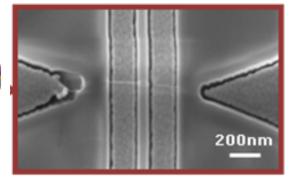
Organic solvent Chemical sensors; Bio sensors Modifications can lead to organic vapor sensors

Kim, Sonkusale, Busnaina, Dokmeci, et al. Nanotechnology, 21 (2010)



## **Carbon Nanotube NEMS Switch**





#### Applications

- Memory element
- Logic gates
- Latches; Registries
- Analog devices
- Operational Amplifiers
- > Sensors

### State I Noltage ON Voltage ON V<sub>cr</sub> (a) $V_{cr}$ (b) $V_{cr}$ $V_{cr}$

Schematic of states I and II.

- ➢ Nano electromechanical Switch
- Non-voaltile
- Bistable Latch
- Position –Alternative state variable
- Novel State Variable Based Logic
- Fabrication employs field assisted directed assembly technique & a single mask process
- CMOS compatible

#### **Characteristics:**

- Read write erase time ~ns
- Read write erase power ~ 100nW
- Infinite sub-threshold slope
- Zero leakage current
- Performance increases with scaling down



## **Carbon Nanotube FET**

SWN'

SiC Si Back-gate

Vg(V)

High Speed binary transistors ;

Power amplifiers; Sensors, etc.

Logic gates; Analog devices;

Source

100n

10n

1n-

10p 1p 100f

-10

**Applications** 

(**A**) pl

Drain

Vd=0.1V

Vd=0.2V

Vd=0.4V

Vd=0.6V

Vd=0.8V

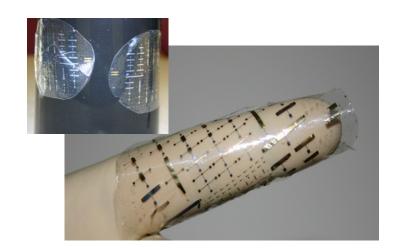
Vd=1.0V

10

#### **Employs Field Assisted Directed Assembly Technique**

Typical p-type behavior, i.e., transistors turn under negative bias.

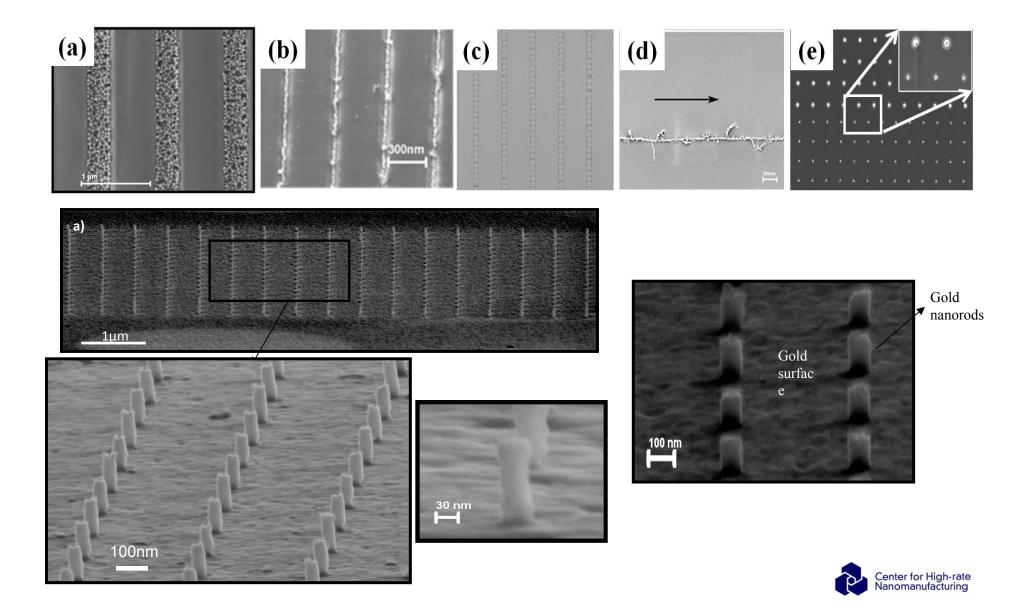
- > The devices show a high  $I_{on/off} > 10^5$
- $\succ$  Low off current  $\sim$  pA
- > Sub-threshold swing of ~ 250 mV/dec.



Selvarasah, Li, Busnaina, and Dokmeci, Appl. Phys. Lett. 97, 1 2010.

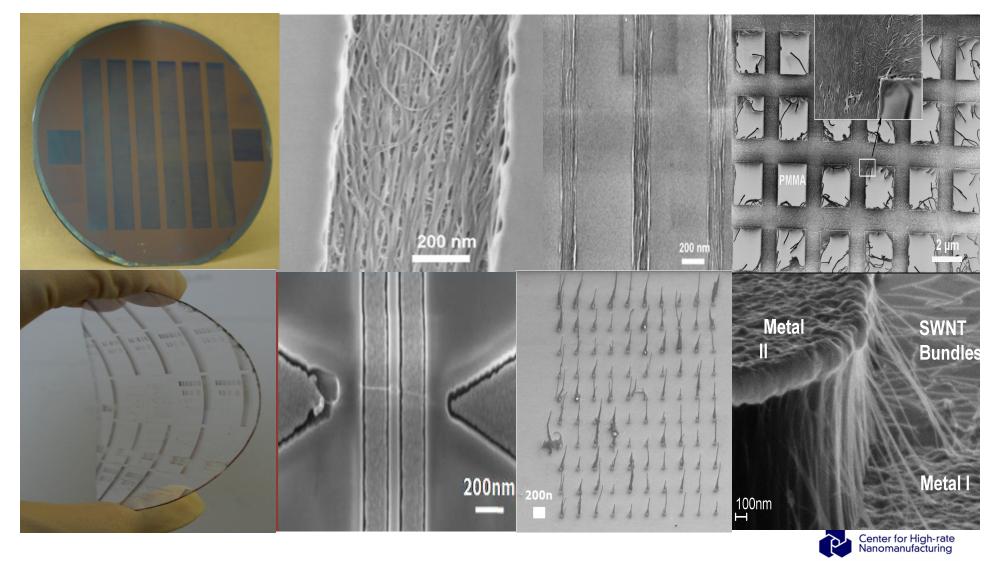


## Assembly of Nanoparticles

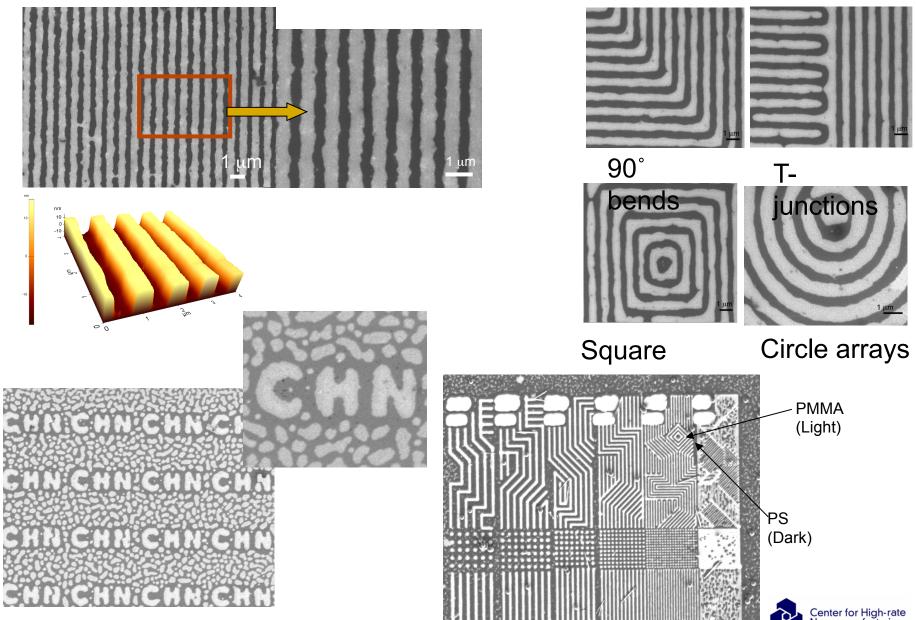


## Assembly of Carbon Nanotubes

Carbon nanotubes assembled in various configuration via various assembly methods

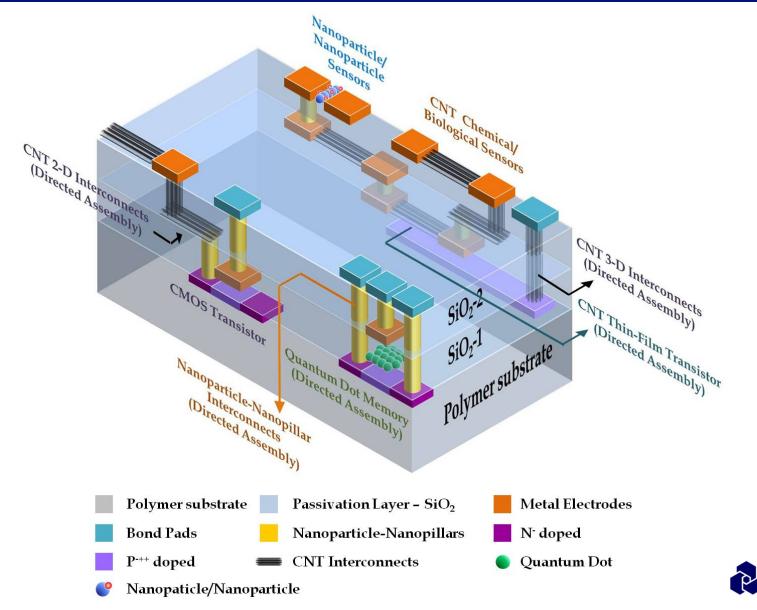


#### **Directed Heterogeneous Assembly of Polymers**



Center for High-rate Nanomanufacturing

### Monolithic Flexible IC - Directed Assembly – Reel to Reel or Batch





### **Strong Industrial Partnerships**



#### Over 30 companies



## Summary

Directed assembly based nanomanufacturing will spur growth by drastically lowering the entry barrier to the fabrication of nanoscale devices.

Many of the potential products that utilize nanomaterials (nanotubes, nanowires, nanoparticles) cannot be commercialized without nanoscale directed assembly.

➤ A nanofactory could be built for \$25-\$50 million, a small fraction of today's cost, making nanotechnology accessible to millions of new innovators and entrepreneurs and unleash a wave of creativity in the same way as the advent of the PC did for computing.

Dramatically lowering such barriers would lead to the creation of entirely new industries.





